Annual report of Research Institute of Electrical Communication 2008

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1. Preface

The Research Institute of Electrical Communication (RIEC) has continued to excel as the only research institute affiliated with a national university that addresses information technology since its establishment in 1935. In 1994, our research institute became one of the National Centers for Cooperative research, addressing “theory and applications of intelligent information science and communication technology”. In the last several decades Information science and communication technology have rapidly developed and brought about great social changes including globalization of communication and physical distribution.

To meet the needs and demands of a new age, in 2004 we reorganized our institute into four research divisions, two research facilities and one research center. Research center for 21st century information science and technology has been established to realize the rapid commercialization of technologies developed at the institute within a 5 years horizon. Two research facilities with 8 research groups working on next generation technology have been established with a 10 years horizon. The Laboratory Nanoelectronics and Spintronics, one of the facilities, is carrying out fundamental research into high-speed semiconductor devices and advanced nano/spin electronics. The Laboratory for Brainware Systems is working towards a new computing system, in which human-being functions are incorporated into information communication technology. The Four Research Divisions are working on basic research for information communication with a 20 years horizon. The Information Devices Division carries out research into materials and devices for communication technology, whilst the Broadband Engineering Division focuses on the development of new technologies for the transmission and storage of vast quantities of information. The human Information Systems Division researches into intelligent information processing and the Systems and Software Division is developing advanced system software for the next generation information society.

The organization of the research institutes and research centers affiliated with national universities has been changed from this school year. Originally, the selected research institutes as a National Center for cooperative research were designed to provide the opportunities to use their excellent facilities and equipments for the researchers in Japan. This change of organization of the institutes is aimed to further activate the cooperative research, so the selected institutes as a COE should meet the needs and demands of the community of the worldwide researcher. Since 1994 our
institute as a National Centers for Cooperative research has already assumed the leadership in creating new technologies through national and international collaboration with researchers in the field of information and communication technology. Our research activities until now might be regarded as an anticipation what this change of organization will aim at. Though we will continue our current research style for a while, we have reorganized our institute every ten years through the self-rating and the external-rating and will make every effort to reform for better contribution. In addition to research activities, we have a mission to train researchers and engineers to internationally high standard through close co-operation with the five departments in the fields of electrical communications at Tohoku University’s School of Engineering and the Graduate School of Information Sciences.

We have published an annual report every year since 1994 summarized our own activities, and distribute it researchers working in the field of information sciences and technology. It is also utilized as self-checking and self-evaluation materials. Please let me hear your frank criticism.

Masafumi YANO, Ph.D.
Director, Research Institute of Electrical Communication.
2. Organization Chart

- Director
- Advisory Council
- Deputy Director
- Research Divisions
  - Information Devices Division
    - Nano-Photoelectronics
    - Quantum-Optical Information Technology
    - Solid State Electronics
    - Dielectric Nano-Devices
    - Materials Functionality Design
    - Magnetic Devices
  - Broadband Engineering Division
    - Ultrahigh-Speed Optical Communication
    - Applied Quantum Optics
    - Wireless Info Tech
    - Information Storage Systems
    - Ultra-Broadband Signal Processing
    - Basic Technology for Broadband Communication
  - Human Information Systems Division
    - Electromagnetic Bioinformation Engineering
    - Advanced Acoustic Information Systems
    - Visual Cognition and Systems
    - Interdisciplinary Field for Informatics
    - Ubiquitous Communications System
    - Multimodal Computing
  - Systems & Software Division
    - Software Construction
    - Computing Information Theory
    - Communication Network Systems
    - Information Content
    - Information Social Structure
  - Laboratory For Nanoelectronics and Spintronics
    - Atomically Controlled Processing
    - Semiconductor Spintronics
    - Nano-Molecular Devices
    - Nano-Spin Memory
  - Laboratory for Brainware Systems
    - Real-World Computing
    - Intelligent Nano-Integration System
    - Microarchitecture
    - New Paradigm VLSI System
  - Research Center for 21st Century Information Technology
  - Project Planning Division
  - Technology Development Division:
    - Mobile Wireless Technology Group
    - Storage Technology Group
    - Intelligence Archive Group
  - Management Office for Safety and Health
  - Flexible Information System Research Center
  - Fundamental Technology Center
  - Machine Shop Division
  - Evaluation Division
  - Process Division
  - Software Technology Division
  - General Affairs Group
  - Cooperative Research Section
  - Library Section
  - Accounting Group
  - Accounting Section
  - Purchasing Section

- Faculty Council
3. RESEARCH ACTIVITIES
Targets and achievements of the Information Devices Division

The main aim of the information devices division is to create new materials and devices for next generation communication technology.

To accomplish the goals of this division, we have the following 6 sub-divisions with different research fields. Furthermore, we also have a partnership with atomically controlled processing research section in the Laboratory for Nanoelectronics and Spintronics.

1. Nano-Photoelectronics
2. Quantum-Optical Information Technology
3. Solid State Electronics
4. Dielectric Nano-Devices
5. Materials Functionality Design
6. Magnetic Devices (Visitor Section)

The target and the summary of achievements of the each sub-division in 2008 are described in the following pages. In addition, about the summary of achievement of the atomically controlled processing research section will be written in the chapter of the Laboratory for Nanoelectronics and Spintronics.
Nanophotoelectronics
Exploring optical and electronic properties of nanometer-sized structures and their applications in photoelectronic devices

[Research Target and Activities]

Our main interest lies in studying the physical and chemical phenomena that take place in nanometer-scale regions and their applications in photoelectronic devices. We investigate the material properties of nanostructures through their optical responses to the local excitation induced by electrons from the tip of a scanning tunneling microscope (STM), as illustrated in Fig. 1. Some chalcogenide alloys including Ge$_2$Sb$_2$Te$_5$ and Sb$_2$Te$_3$ attract strong attention because of their potential for data-storage devices with ultra-high capacity. Hence it is highly desirable that their optical, thermal, and electronic properties are explored and understood with nanometer scale spatial resolution, i.e., the resolution comparable to targeted sizes of devices. We have observed STM light emission (STM-LE) spectra of Ge$_2$Sb$_2$Te$_5$ and Sb$_2$Te$_3$. Although these chalcogenide alloys exhibit band gaps less than 0.5 eV, the STM-LE was observed with a narrow spectral width at a photon energy of 1.5 eV for both materials. We found that the STM-LE is excited by electronic transitions taking place in the local electronic structure having a direct gap-like shape with a band gap of 1.5 eV. It is also important to understand temporal behaviors of phonons in individual nano structures from a view point of nano device engineering. Hence we are interested in adding pico-second temporal resolving power to STM-LE spectroscopy. By exciting STM-LE with the tunneling electrons induced by pico-second laser pulses, we have succeeded in detecting vibrations of individual isolated single benzene molecules adsorbed on a Cu(110) surface and optical phonons of Sb$_2$Te$_3$ with pico-second temporal resolution.

[Staff]
Professor  UEHARA, Yoichi Dr.
Assistant Professor  KATANO, Satoshi Dr.

[Profile of Professor UEHARA]
Dr. Yoichi Uehara obtained his D. Eng. degree from the Department of Engineering, University of Osaka prefecture in 1986, after which, he was initially appointed as an Assistant Professor at the Research Institute of Electrical Communication, Tohoku University. He eventually became a Full Professor at the institute in 2005. Dr. Uehara has worked on three main surface physics problems at Tohoku University: (1) light emission from metal-insulator-metal (MIM) and metal-oxide-semiconductor (MOS) tunnel junctions, (2) low-energy electron spectroscopy, and (3) light emission spectroscopy of scanning tunneling microscope (STM).

[Papers]
Quantum Optical Information Technology

Development of optoelectronic devices for quantum information and communication technology

[Research Target and Activities]
Current information and communication technology utilizes macroscopic and classical physical quantities, such as voltage or frequency of electric fields. The classical technology will reach the limit of information density and speed in the near future. The quantum-mechanical counterpart, “quantum information processing and communication technology”, in which information is carried by microscopic and quantum-mechanical quantities, is expected to overcome the difficulty.

Our goal is to develop the quantum information devices utilizing quantum interaction between electrons and photons in semiconductor nanostructures, to get further understanding of their physics, and to apply them to practical quantum information technologies. We are particularly working toward the development of future quantum information devices utilizing entangled photon pairs and electron spins in semiconductor nanostructures.

In 2008, we have achieved (1) development of a novel scheme to generate and detect entangled photons, (2) measurement of single-photon-level optical nonlinearity in an optical fiber, and (3) tomographic measurement of optically injected electron spins in a semiconductor.

[Staff]
Professor: EDAMATSU, Keiichi Dr.
Associate professor: KOSAKA, Hideo Dr.
Assistant professor: MITSUMORI, Yasuyoshi Dr.

[Profile of Professor EDAMATSU]
Keiichi Edamatsu received B.S., M.S., and D.S. degrees in Physics from Tohoku University. He was a Research Associate in Faculty of Engineering, Tohoku University, a Visiting Associate in California Institute of Technology, and an Associate Professor in Graduate School of Engineering Science, Osaka University.

[Papers]

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Solid State Electronics Laboratory

Ubiqitous Silicon Technology

[Research Target and Activities]
To realize the ubiquitous (or ambient intelligence) society, in which sensors and their networks are embedded in our ambience to support our daily life, a marriage between non-Si technologies suitable for environmental sensing and the Si technology suitable for signal processings is indispensable. To this goal, we investigate formation of ultrathin silicon-carbide (SiC) films on Si substrates, hoping to use them as a common interface between the two technologies. SiC is a group-IV compound that contains a pair of elements representative of both electronics (Si) and biology (C). It also bridges the gap between Si and other II-VI or III-V compounds. It is a widegap semiconductor that enables high-temperature operations. High enough strength and hardness of SiC make this material suitable for use in MEMS structures. We are developing gas-sensors, graphene-based ultrahigh-speed devices, LEDs, biosensors, MEMS structures, non-volatile memories, and photovoltaic cells based on the SiC/Si structures. What lies behind these applications is our original technology of SiC gas-source molecular-beam epitaxy (MBE) using organo-silane, which enables a high-quality, low-temperature SiC epitaxy on Si substrates. One of our most striking achievements in this field is the realization of the graphene-on-silicon structure, in which graphene, a most promising semiconducting material to be used in the next generation devices, is formed by use of surface modification of SiC/Si. Our research also covers the surface chemistry of Si-related surfaces, targeted to the control of nanostructure formation on Si and SiC surfaces. Fabrication of non-equilibrium Si structures such as amorphous, microcrystalline, and poly-Si thin films is also within our interests, and is being intensively investigated using atmospheric-pressure plasma-enhanced chemical vapor deposition (AP-PECVD).

[Staff]
Professor: SUEMITSU, Maki Dr.
Assistant Professor: FUKIDOME, Hirokazu Dr.
Postdoctoral Fellow: ALGUNO, Arnold Café Dr.

[Profile of Professor Suemitsu]
Prof. Maki Suemitsu obtained bachelor degree on electronic engineering (1975), Ph.D on electronic engineering (1980). He started his service at Research Institute of Electrical Communication (RIEC) as research associate (1980), and became associate professor (1990). He then became professor at Center for Interdisciplinary Research (2003). Since 2008, he has been professor at RIEC. He has been engaged mainly on surfaces of semiconductor thin films. He was awarded the 30th Kumagai prize of the best paper from the Vacuum Society of Japan.

[Papers]
3. Initial oxidation of Si(100)-2x1 as an autocatalytic reaction, Physical Review. Lett., 82(1999), pp.2334-2337. Maki Suemitsu, Yoshiharu Enta, Yasushi Miyanishi, and Nobuo Miyamoto

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Dielectric Nano-Devices
Research on Dielectric Nano Science and Technology

[Research Target and Activities]
Our main area of interest is evaluation and development of dielectric materials, including ferroelectric and piezoelectric materials and their application to communication devices and ferroelectric data storage systems. Our major contributions to advancement in these fields are the invention and the development of “Scanning Nonlinear Dielectric Microscope” (SNDM) which is the first successful purely electrical method for observing the ferroelectric polarization distribution without the influence of the shielding effect by free charges and it has already been put into practical use. The resolution of the microscope has been improved up to atomic scale-order. Therefore, it has a great potential for realizing the ultra-high density ferroelectric recording system.

Major achievements of studies in 2008 are as follows: (1) Surfaces of various material (cf. silicon, fullerene molecule, oxides) were observed with atomic resolution using non-contact SNDM. (2) Digital information data were recorded on ferroelectric single-crystal recording media with an areal recording density of 4 Tbit/inch² using an SNDM-based storage system.

[Staff]
Professor : CHO, Yasuo Dr.
Assistant Professor : HIRANAGA, Yoshiomi Dr.
Assistant Professor : KIN, Nobuhiro
Technical Official : WAGATSUMA, Yasuo
Research Fellow : ISHIKAWA, Kenya Dr.
Research Fellow : KOBAYASHI, Shinichiro Dr.
Research Fellow : BHUIYAN, Moinul Dr.
Research Fellow : ODAGAWA, Nozomi

[Profile of Professor Cho]
Yasuo Cho graduated in 1980 from Tohoku University in electrical engineering department. In 1985 he became a research associate at Research Institute of Electrical Communication Tohoku University. In 1990, he received an associate professorship from Yamaguchi University. He then became an associate professor in 1997 and a full professor in 2001 at Research Institute of Electrical Communication Tohoku University. During this time, his main research interests included nonlinear phenomena in ferroelectric materials and their applications, research on the scanning nonlinear dielectric microscope, and research on using the nonlinear dielectric microscope in next-generation ultrahigh density ferroelectric data storage (SNDM ferroelectric probe memory).

[Papers]
Materials Functionality Design

Computational Design of Functional Materials for Spintornics Devices

[Research Target and Activities]
Our research targets are as follows: (1) theoretical analyses of quantum phenomena which appear in materials and nanostructures for advanced information devices, (2) computational design of materials and nanostructures which possess new functionalities for improvement of device performance, and (3) development of new design procedures based on large-scale computational simulation techniques.

Our research activities in FY 2008 include computational materials design for spintornics as follows: (1) Magnetic tunnel junctions (MTJ) with perpendicular magnetic anisotropy
In order to ensure enough endurance against thermal fluctuations in downsizing non-volatile magnetoresistive random access memory (MRAM), it is inevitable to adopt ferromagnetic materials with large magneto-crystalline anisotropy as electrodes of the MTJ. We investigate the effect of interfacial Fe layers into the FePt/MgO/FePt (001) MTJ on their tunneling magnetoresistance (TMR) ratio on the basis of first-principles calculations. The TMR ratio increases dramatically even for 3-4 Fe layers inserted into both sides of the MgO barrier and the FePt electrodes.

(2) Highly spin-polarized interfaces between Heusler alloys and oxides
We investigate the electronic and transport properties of MgO-based MTJ with half-metallic Heusler alloy electrodes on the basis of first-principles calculations. The insertion of thin Co2CrAl layers between Co2MnSi electrodes and the MgO barrier can eliminate electronic states in the minority-spin gap at the interface, leading to 100% spin polarization at the Fermi level in the interfacial region (Fig. 1). The insertion of thin Co2CrAl layers into the MTJ can improve the TMR ratio at room temperature.

[Staff]
Professor : SHIRAI, Masafumi Dr.
Assistant Professor : MIURA, Yoshio Dr.
Assistant Professor : ABE, Kazutaka Dr.

[Profile of Professor SHIRAI]
Masafumi Shirai was received the Doctor of Engineering degree from Osaka University in 1989. From 1988 to 1996, he was a Research Associate, and then an Associate Professor at Osaka University. From 2002 to the present, he has been a Professor at Tohoku University. His research interests have been itinerant electron magnetism in transition-metal compounds, computational materials design on the basis of first-principles calculations, and so on.

[Papers]

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Broadband Engineering Division: Research Target and Results

In order to establish the future broadband communication systems and novel devices that are flexibly applied to the future ubiquitous ultra-large capacity information communication, research and development are carrying out over the wide bands of microwaves, millimeter/submillimeter waves, terahertz waves, and lightwaves with regard to the information generation, transmission, processing, storage, and semiconductor spintronics technologies.

(1) Wireless Information Technology

Research and development are progressing toward Dependable Wireless Next-generation-network (DWN) in the 21st century's wireless information technology. For realizing the DWN, we are interested in developing the following wireless network: (i) next-generation mobile broadband wireless access (MBWA), (ii) dependable broadband wireless local area network (WLAN), and (iii) ultra-broadband wireless personal area network (WPAN).

We are also actively engaged in work on following technology for broadband, low-power consumption and small-size terminals: RF power amplifier, synthesizer and mixer devices for millimeter-wave-band wireless modems, and GHz-band film bulk acoustic resonator (FBAR) filters and oscillators.

(2) Ultra-Broadband Signal Processing

We are developing novel, integrated electron devices and circuit systems operating in the terahertz region. One example is a plasmon-resonant emitter structured by dual-grating gates high-electron mobility transistors. The microchip emitter was introduced as a broadband terahertz light source into a spectrometer and successfully measured the water-vapor absorption spectra over the terahertz range.

We are also pursuing new materials called “graphene,” a single-layered honeycomb lattice carbon crystal, featuring extraordinary carrier transport properties of massless Dirac Fermions. We have succeeded in transistor operation by introducing graphene as a channel material in a Si-based transistor.

(3) Ultrahigh-Speed Optical Communication

To achieve a global high-capacity optical network, we have been engaged in the research on ultrahigh-speed Optical Time-Division Multiplexing (OTDM) transmission and highly spectral-efficient coherent Quadrature Amplitude Modulation (QAM) transmission.

As regards the ultrahigh-speed transmission, 320 Gbit/s DPSK signal was successfully transmitted over 525 km with a time-domain optical Fourier
transformation (OFT) technique. In coherent transmission, we successfully demonstrated a 6 channel FDM transmission of 64 QAM signals over 160 km, in which a total capacity of 72 Gbit/s was transmitted with only 8.4 GHz bandwidth and thus the spectral efficiency reached as high as 8.6 bit/s/Hz.

(4) Applied Quantum Optics

Novel functional semiconductor photonic devices are investigated to explore new generation photonic network systems. Ultra-broadband coherent terahertz (THz) wave radiation, called as “Tera-Photonics,” is also studied.

Research for ultra-high-speed semiconductor photonic devices operated by controlling signal light injection has been started. Novel THz biosensors using surface plasmon resonance are also studied to achieve sensitive detection of biomolecules and THz imaging with sub-wavelength spatial resolution. Local-field enhancement is clarified from numerical analysis and suitable structures for the resonators are clarified.

(5) Information Storage Systems

Amount of digital information is rapidly growing year by year in this decade. High density magnetic recording technology to store the tremendous volume of information is crucial to meet this strong demand. System technology to process data access to/from the disks plays another significant role.

For a high areal density of 2 Tbits/inch², a computer simulation based on LLG equation and experimental studies for high density read/write performance of perpendicular magnetic recording have been carried out. The recording method based on bit patterned media and its design guideline were thus established. A novel storage architecture to reduce power consumption was developed for petabyte-class large-capacity systems.

(6) Basic Technology for Broadband Communication (Mizuno Lab.)

Development of measurement instruments using the millimeter- and terahertz-wave region of the electromagnetic spectrum is the research target of this laboratory.

We are developing imaging systems mainly of passive mode, which enables non-invasive measurement possible. A 2-dimensional imaging array for the 35 GHz-band has been developed and the imaging speed of 0.8 s/frame has been obtained.

(7) Basic Technology for Broadband Communication (Inutake Lab.)

We are interested in developing air-borne synthetic aperture radars (SAR) for civilian applications contributing safety and security improvement. The SAR will be useful for all-weather surveillance and rescue in disastrous fires and smokes.
Conceptual design of a spotlight-mode SAR system has been done, targeting at a high resolution (5-10 cm), small size and light weight (20-30 kg) at Ku/Ka-band. Scientists and engineers from both universities and industries collaborate on the program.

(8) Laboratory for Nanoelectronics and Spintronics: Semiconductor Spintronics

The nanoscience and nanotechnology to control the quantum states in semiconductors, especially the spin-states and optical transitions, are investigated to realized new functional devices, such as memories and logic devices using spin states as well as quantum cascade lasers (QCL) with THz emission.

Control of magnetic anisotropy in a (Ga,Mn)As film was demonstrated by the application of electric-fields. Phase coherence control of spin-3/2 $^{75}$As nuclei was demonstrated in a GaAs/AlGaAs quantum well by multipulse NMR sequences and optically-detected NMR. GaAs-based quantum cascade lasers emitting at 3.8 THz was realized, and intersubband transitions in ZnO quantum wells was observed.

(9) Laboratory for Nanoelectronics and Spintronics: Nano-Spin Memories

Technologies to realize advanced spin memory and logic devices using magnetic tunnel junctions (MTJs) consisting of ferromagnetic metal electrodes and insulating barriers are developing.

The TMR ratio of 604 (1144) % at RT (5 K) (world record) was demonstrated in MgO-based MTJs. Low intrinsic critical current density ($J_0$) without degrading the thermal-stability factor $E/k_BT$ was realized by using SyF free layer. Fabrication process of magnetic tunnel junction (MTJ) on circuits was developed, and the operation of nonvolatile full adder based on logic-in-memory architecture was verified.
Research Area of Ultrahigh-Speed Optical Communication

Advanced optical communication technologies approaching the Shannon limit

[Research Target and Activities]

With the vast growth of traffic on the Internet from simple text data to high quality voice, images, and real-time video, it has become increasingly important to realize a high-capacity and high-speed network to support the daily needs of modern communications. Ultrahigh-speed optical communication is the key technology for building such an interconnected world. This laboratory aims to achieve a global ultrahigh-speed optical network in the 21 century by engaging in the research of ultrashort pulse generation and transmission.

For ultrahigh-speed transmission, we have developed a new distortion-free transmission scheme which employs the optical Fourier transformation (OFT) in the time domain. This year, we applied this technique to 320 Gbit/s system, and successfully demonstrated error-free transmission over 525 km, in which tolerance to higher-order polarization-mode dispersion was greatly improved with OFT. We have also been actively engaged in coherent QAM optical transmission for achieving the ultrahigh spectral efficiency approaching the Shannon limit. This year, we successfully demonstrated a frequency-division multiplexed 64 QAM transmission, in which polarization-multiplexed 1 Gsymbol/s, 64 QAM (12 Gbit/s) data were successfully transmitted over 160 km in a 6-channel FDM with a 1.4 GHz spacing. Thus the spectral efficiency reached as high as 6 bit/s/Hz with a total capacity of 72 Gbit/s (Fig. 1).

[Staff]

Professor: NAKAZAWA, Masataka Dr.
Associate Professor: HIROOKA, Toshihiko Dr.
Assistant Professor: YOSHIDA, Masato Dr.
COE Fellow: KASAI, Keisuke Dr.

[Profile of Professor NAKAZAWA]

Prof. Nakazawa received a Ph. D. degree from the Tokyo Institute of Technology in 1980. In 1980, he joined the Ibaraki Electrical Communication Laboratory, Nippon Telegraph & Telephone Public Corporation. He was a visiting scientist at Massachusetts Institute of Technology in 1984-1985. In 2001, he moved to the Research Institute of Electrical Communication, Tohoku University as a professor, where he has been engaged in research on ultrahigh-speed optical communication including soliton transmission, nonlinear effects in fibers, mode-locked lasers, and photonic crystal fibers. He is the author and coauthor of over 380 journal articles and holds more than 100 patents. He is a Fellow of IEEE, OSA, and IEICE, and has received various awards including the 2002 Daniel E. Noble Award and 2005 OSA R. W. Wood Prize.

[Papers]


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Applied Quantum Optics
Research on Innovative Highly-Functional Photonic Semiconductor Devices and Ultra-wide band coherent light sources, and their applications

[Research Target and Activities]
Novel functional semiconductor photonic devices including photonic integrated circuits are investigated to explore new-generation photonic network systems. We have also been studying ultra-broadband coherent terahertz (THz) wave radiation to explore novel science and technology, “tera-photonics”.

Ultra-high-speed semiconductor photonic active devices operated by control signal light injection are started to research by utilizing a semiconductor laser with an external feedback cavity. In order to realize highly-functional photonic semiconductor modulators, coherent optical signal generation is successfully demonstrated by using compact and low-driving-voltage semiconductor Mach-Zehnder modulators. Novel THz biosensors using surface plasmon resonance are also studied to achieve sensitive detection of biomolecules, high-spatial-resolution THz imaging beyond diffraction limit, and application to THz-frequency coherent anti-Stokes Raman (THz-CARS) microscopy system we developed. Local-field enhancement was numerically analyzed to clarify optimum design of resonator structure.

![Experimental setups for novel semiconductor photonic functional devices (left), and THz-CARS (center) and its image for poled lithium niobate (right)](image)

[Staff]
Professor : YASAKA, Hiroshi Dr.
Associate Professor : SHIKATA, Jun-ichi Dr.

[Profile of Professor YASAKA]
Hiroshi YASAKA received the B.S. and M.S. degrees in physics from Kyusyu University in 1983 and 1985, and Ph.D. degree in Electronics Engineering from Hokkaido University in 1993. In 1985 he joined the Atsugi Electrical Communications Laboratories, Nippon Telegraph and Telephone (NTT) Corporation. Since then, he has been engaging in research and development on semiconductor photonic devices for optical fiber communication systems. From 2008 he has been a professor of Tohoku University. He is a member of the Institute of Electronics, Information and Communication Engineers (IEICE) of Japan, the Japan Society of Applied Physics, the Physical Society of Japan, and IEEE/LEOS.

[Papers]
Wireless Info Tech  
For Realizing Dependable Wireless Next-Generation-Network (DWN)

[Research Target and Activities]
Research and development are progressing toward Dependable Wireless Next-Generation-Network (DWN) in the 21st century's wireless information technology. The DWN immediately provides any information for everyone at anytime and anywhere.

For realizing the DWN, we are interested in developing following wireless network: (1) next generation mobile broadband wireless access (MBWA), (2) dependable broadband wireless local area network (WLAN), and (3) ultra broadband wireless personal area network (WPAN).

In this year, we have discussed following technology for broadband, low power consumption and small size terminals: (1) RF power amplifier, synthesizer and mixer devices for millimeter wave and GHz-band wireless modems, (2) GHz-band film bulk acoustic resonator (FBAR) devices, (3) Ultra small antennas for mobile terminals, (4) Seamless interconnection technology using 3-dementional system in package (3D SiP), and (5) frequency domain equalizer (FDE) devices on field programmable gate array (FPGA). Moreover, we have evaluated the MBWA field trial using frequency hopping orthogonal frequency division multiple access (FH-OFDMA) system.

[Staff]
Professor: TSUBOUCHI, Kazuo, Ph. D
Associate Professor: NAKASE, Hiroyuki, Ph. D (~June 2008)
Assistant Professor: KAMEDA, Suguru, Ph. D

[Profile of Professor: TSUBOUCHI]
Prof. Tsubouchi received the Ph.D. degree in Electronics Engineering from Nagoya University in 1974. Since 1974, he has been with the Research Institute of Electrical Communication, Tohoku University. In 1982, he spent at Purdue University as a Visiting Associate Professor. He is currently the professor and the director of IT-21 Center. He received the 2005 Achievement Award from the IEICE, and “Minister of Education, Culture, Sports, Science and Technology, Award” in the Award for Persons of Merit in Industry-Academia-Government Collaboration in FY2007, et al. He is a member of the IEEE, the IEICE, the Physical Society of Japan, the Japan Society of Applied Physics, and the Institute of Electrical Engineers of Japan.

[Papers]

Fig. The target system of Wireless Info Tech.
Research on Large Capacity Information Storage System Using Perpendicular Magnetic Recording

[Research Target and Activities]

Amount of digital information is rapidly growing year by year, which is projected to exceed 100 Exa-byte \((10^{20} \text{ bytes})\) in 2010. Extremely large storage capacity by high density magnetic recording for the information is thus required. Perpendicular magnetic recording was introduced in order to continuously develop areal density of hard disk drives beyond the conventional density limit, 1 Tbit/inch\(^2\) and even up to 5 Tbit/inch\(^2\). We thus focus on research on high density perpendicular magnetic recording in next generation. Theoretical studies including a micromagnetic computer simulation in association with experimental approach are carried out for this purpose.

As we early proposed, magnetic nano-structure of recording media is the most essential parameter to achieve high density perpendicular recording. Bit-patterned medium (Fig 1 and 2) is one of promising candidates. For a target areal density of 2 Tbit/inch\(^2\) in which the bit size corresponds to the area of 13 nm by 25 nm, recording error-rate performance for the bit-patterned medium was analyzed. A design guideline of the writing process for the media was clarified based on the statistical modeling. The analyses unveiled geographical and magnetic uniformity of the patterned dots was found to be essential, as well as the writing head resolution. The computer simulation derived concrete specifications of heads and media for high density recording.

In addition to the studies on magnetic recording, a novel low-power consumption architecture for extremely large capacity storage system was developed based on tiered operation of hard disk drives.

[Staff]
Professor: MURAOKA Hiroaki Dr. (since 2000)
Associate Professor: Simon J. GREAVES Dr. (since 2003)
Assistant Professor: MIURA Kenji, Dr. (since 2003)
Secretary: KASUYA Sachiko

[Profile of Professor: MURAOKA]
In 1991, Professor Muraoka joined Tohoku University. Since then, he has been engaged in research on high-density magnetic recording devices, systems and recording theories, mainly for perpendicular magnetic recording. He received PhD degree in 1981. He is a Fellow of IEEE.

[Papers]

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Ultra-broadband Signal Processing

Novel Millimeter-wave and Terahertz Integrated Electron Devices and Systems

[Research Target and Activities]

We are developing novel, integrated electron devices and circuit systems operating in the terahertz region. One example is a plasmon-resonant terahertz emitter structured by dual-grating gates high-electron mobility transistors (HEMT’s). The microchip emitter was introduced as a broadband terahertz light source into a spectrometer and successfully measured the water-vapor absorption spectrum in the self-oscillation mode at room temperature in comparison with that for a standard high-pressure mercury lamp, right: water-vapor absorption spectrum measured by using the microchip emitter (upper) in comparison with the reference data provided by NASA (lower.)

Graphene, a single-layered honeycomb lattice carbon crystal, features extraordinary carrier transport properties due to the nature of massless Dirac Fermions. We have succeeded in transistor operation by introducing graphene as a channel material in a Si-based transistor, leading to terahertz operation.

[Staff]
Professor: OTSUJI Taiichi Dr.
Associate Professor: SUEMITSU Tetsuya, Dr.
Assistant Professor: Yahya Moubarak MEZIANI, Dr.
Visiting Professor: Wojciech KNAP, Dr.
JSPS Research Fellow: Abdelouahad EL FATIMY, Dr.
Secretary: HAMADA Megumi

[Profile of Professor OTSUJI]
Prof. Taiichi Otsuji received the B.S. and M.S. degrees in electronic engineering from Kyushu Institute of Technology (Kyutech), Fukuoka, Japan, in 1982 and 1984, and the Ph.D. degree from Tokyo Institute of Technology, Tokyo, Japan, in 1994. After working for NTT Laboratories, Japan, since 1984, he joined Kyutech in 1999, as an associate professor, being a professor from 2002. Since 2005, he has been a professor at RIEC, Tohoku University, Japan. He is the recipient of the Outstanding Paper Award of the 1997 IEEE GaAs IC Symposium. He is a member of IEEE, OSA, IEICE, and JSAP.

[Papers]

Contact to Professor Taiichi OTSUJI : otsuji@riec.tohoku.ac.jp
Basic Technology for Broadband Communication
Development of Measurement Instrumentation by the Millimeter and THz Wave

[Research Target and Activities]
Development of measurement instruments using the millimeter- and terahertz- wave region of the electromagnetic spectrum is the research target of this Division. Since the wavelength of this region is larger than that of the infrared and optical region, scattering by cloud, dust, flame, fabrics, skin, etc. is much smaller and since photon energy of this region is much smaller than thermal energy $kT_B$ at the room temperature, non-invasive measurement for materials is possible. We are developing imaging systems mainly of passive mode and mainly for security applications.

[Left]: 35 GHz 2-dimensional (7 x 7) imaging array developed. [Right]: Measured depth of field. Vertical axis shows size of images (arbitrary unit) and the figure is plotted as a function of distance from lens (horizontal axis. mm).

[Staff]
Visiting Professor: MIZUNO Koji Dr.

[Profile of Professor MIZUNO]
Koji Mizuno was graduated from the Department of Electronic Engineering, Tohoku University, Sendai, in 1963 and was appointed Professor at the University in 1984. He was a visiting researcher at the University of London in 1972, and at California Institute of Technology in 1990. In 2004 he was appointed Professor Emeritus of Tohoku University and also Research Professor of RIEC. He is recipient of the IEEE Fellow grade in 1993 (now Life Fellow), the Kenneth J. Button Medal in 1998, the Minister Award of MEXT (Ministry of Education, Culture, Sports, Science and Technology, Japan) in 2003, and IEEE MTT-Society Distinguished Educator Award in 2005.

[Papers]

Contact to Professor Koji MIZUNO : koji@riec.tohoku.ac.jp
Basic Technology for Broadband Communication

High Resolution Synthetic Aperture Radar for Civilian Applications

[Research Objectives and Activities]
We are interested in developing air-borne synthetic aperture radars (SAR) for civilian applications. We intend contributing safety and security improvement of the country. The SAR will be useful for all-weather surveillance and rescue in disastrous fires and smokes. Scientists and engineers from both universities and industries collaborate on our research and development programs.

1. Conceptual design of a spotlight-mode SAR system has been done, as shown in Fig.1.
2. We aim for developing high resolution (5-10cm), small size and light weight (20-30kg) SAR at Ku/Ka-band microwave frequencies.
3. Domestically-available, manned and unmanned aerial vehicles, which are suitable for the SAR platforms, were identified.
4. A principle that our developments are open to all contributing parties is adopted.

[Staff]
Visiting Professor: INUTAKE Masaaki Dr.

[Profile of Professor INUTAKE]
1966: Bachelor of Aeronautical Engineering, University of Tokyo.
1972: Doctor of Engineering, University of Tokyo.
1972-1974: Research Fellow, Institute of Space and Aeronautical Science, University of Tokyo.
1974-1980: Assistant Professor, Institute of Plasma Physics, Nagoya University
1980-1994: Associate Professor, Graduate School of Applied Physics, University of Tsukuba.
1994-2007: Professor, Graduate School of Engineering, Tohoku University.
2007-present: Visiting Professor, Research Institute of Electrical Communication, Tohoku University.
Researches: Development of dense plasma sources. Microwave/laser-aided plasma diagnostics. Alfvén wave physics and its applications for the wave heating of a fusion plasma. Alfvén wave heating and acceleration of supersonic plasma flows in a magnetic nozzle for an advanced space propulsion.

[Papers]

20 Contact to Professor Masaaki INUTAKE : inutakem@riec.tohoku.ac.jp
Aims and Achievements of Human Information Systems Division

Understanding the mechanisms of human information processing and realizing appropriate information communication environments are necessary for advanced information communication systems.

The aim of this division is to develop elementary and system technologies for the next generation of information communication systems that are in harmony with human and the environment. For the aim, Achievements of Human Information Systems Division covers the research fields of information creation of biological systems, information communication environments optimization and human information processing.

To achieve the aim, the division consists of the following four laboratories: Electromagnetic Bioinformation Engineering, Advanced Acoustic Information Systems, Visual Cognition and Systems, Interdisciplinary Field for Informatics, and Ubiquitous Communications System.

(1) Electromagnetic Bioinformation Engineering
(Aims) The aim of our division are to obtain the high accuracy sensor system for the signals from the human body or electric devices and to obtain the system for approaching action to the human body, by using the magnetically nano-scale controlled magnetic materials and by the development of the devices under the functions of the magnetics.
(Achievements) We proposed a extraordinary high sensitivity of the magnetic field sensor and the strain sensor by the technique of optimizing the properties of the magnetic thin films. The manipulating technique for controlling the coagulation and the dispersion of the magnetic small particles, which will be used in the human body, was proposed. In addition, 3D position detecting system using magnetic markers was studied to improve its position accuracy. The study about the magnetic actuator driven by external magnetic field was carried out for micro-pump, micro-cantilevered actuator, and so on. The system supporting the medical operation under the endoscopes was studied accompanied with the medical doctors.

(2) Advanced Acoustic Information Systems
(Aims) To realize future high-definition communication systems with rich and natural sense of presence, acoustic information processing technologies based on good knowledge of human auditory system as well as multimodal perception relating to hearing are studied.
(Achievements) In 2008, we deepen the understanding human spatiotemporal perceptual processes of audio-visual and audio-vestibular information. This is particularly important to realize future multi-modal information processing and communication systems. We continued to develop methods, such as virtual auditory displays based on our accumulated knowledge of human auditory space perception, new auralization method for concert hall based on sound field simulation and 1/10th-scale measurement, and high-precision analyzing method using a surrounding microphone array, to realize 3D immersive sound fields. They are keenly required to realize super-definition audio-visual communications in near future. Furthermore, we developed new signal processing algorithms of
digital watermarking, safer voice over internet protocols (VoIP) realizing high secrecy, advanced
digital hearing aids, and high-performance binaural speech enhancement providing proper spatial
information.

(3) Visual Cognition and Systems
(Aims) To understand the vision-related brain functions in order to apply the knowledge to realize
human oriented information communication systems.
(Achievements) We achieved results in the fields of visual attention motion perception and motion in
depth perception. Firstly, we succeeded to measure distribution of visual attention while people
tracking a moving object, and the measurements showed that the visual system has difficulty to pay
attention to different location other than the object tracked. This knowledge is useful to model the
function of visual attention. Secondly, we showed that only slow motion detector contribute to
detection of global motion and also that it is different from the attention-based motion system. Since
slow motion signals have been paid little attention both in vision and image processing, the result
provided new view of role of motion signals in visual processing. Finally, we investigated temporal
factors of human depth perception. Most importantly, we revealed that the visual system has two
different mechanisms for seeing motion in depth perception. Using a technique to test the ability to
see motion in depth based on each mechanism, we showed there are two different types of stereo
blind people: ones with problem to see static depth and ones with problem to see dynamic depth
change.

(4) Interdisciplinary Field for Informatics
(Aims) The interdisciplinary field of research combining nanobiotechnology and information
technology attracts our exceeding interests. These studies stand on the development of new materials
created by self-assembly of functional molecules and nanomaterials, leading to new classes of
biosensing devices.
(Achievements) We have developed high density DNA microarrays in combination with
microcontact printing (mCP) and surface plasmon resonance (SPR) imaging. The hybridization
between target DNA immobilized on gold nanoparticles and probe DNA arrayed on flat gold
substrates was successfully demonstrated in high contrast image by the enhancement of optical
signals based on nanoscale phenomena. Tuning of work function of carbon nanotube-based FET
devices as well as characterization of specific interaction between b-cyclodextrin molecules are
performed as the application works with thiolated self-assembled mono-layers. The cell cultures on
nanopatterned block copolymer surfaces and the production of anti-azobenzene IgG in rabbit are also
carried out as a part of nanobio study.

(5) Ubiquitous Communications System
(Aims) To realize communications environments in which everybody can communicate with
anybody, any where and any time by carrying out researches on radio propagation characteristics,
electromagnetic interferences, and radio equipment with which customers can communicate universally.

To contribute to materialize or to lead IEEE standardization based on “Japanese technologies”
(Achievements) To realize Gigabit per second transmission employing millimeter wave (60GHz)
indoor PAN (Wireless Personal Area Networks) systems, a lot of measurements and experiments
have been done on antenna directivity / beam width and delay spreads, reflective waves by walls and
metals (or equivalents) and they lead to the inventions of simultaneous multiple signal path tracking
(including the intentionally created path) and dynamic path selection to increase communications
reliability enhancement. Also, a lot of physical layers have been designed and simulated to develop
robust WPAN systems in various and practical fading environments. Moreover, ultra high speed
MAC (Medium Access Control) has been designed and developed for high throughput Gigabit
WPAN.

Through organizing a Consortium (COMPA, Chair: Professor Shuzo Kato) (22 active
members) and getting a group of supporting COMPA as Partners (17 non-Japanese institutes), a
global and harmonized IEEE standardization has been executed successfully (the standardization will
be finalized in this year). This will be the first successful standardization of “Japanese organization
driven IEEE standardization) and it will a good role model of IEEE standardization for Japanese
industry.
Electromagnetic Bioinformation Engineering

Communication with human body

[Research Target and Activities]

The magnetic field sensor with very high sensitivity was produced. In this work, new processes for controlling the anisotropy of the magnetic thin films were approached and a huge change of the impedance of the sensor was obtained. On the work of wireless magnetic motion capture systems, the factors in the detection accuracy were quantitatively clarified. The magnetic actuators such as the micro-pump, and the cantilevered actuator with the magnetostrictive thin films were studied. The work for the magnetic actuator for the capsule-endoscope was carried out with a company of the medical equipments.

[Staff]
Professor: ISHIYAMA Kazushi Dr. (Since 2007)
Assistant Professor: HASHI Shuichiro Dr. (Since 2008)

[Profile of Professor ISHIYAMA]
He received his MS and PhD degrees in Electrical Engineering from Tohoku University in 1986 and 1993, respectively. He is currently working as a professor in Research Institute of Electrical Communication, Tohoku University. His research interests are in the area of magnetics and magnetic applications.

[Papers]
Advanced Acoustic Information Systems

Development of next generation communication systems

[Research Target and Activities]
Since human beings can be regarded as the extreme source and recipient of information in any communication systems. Therefore, to develop advanced acoustic communication systems, good knowledge of human auditory system as well as multimodal perception relating to hearing is essential. The main interest of this laboratory is thus a study of the information processing in the human auditory system. Moreover, in recent years, we have been devoting a lot of effort to investigate human multi-modal information processing including hearing. We are, at the same time, aiming at the realization of a ‘comfortable’ sound environment exploiting digital signal processing techniques. Three-dimensional sound image control by high-definition virtual auditory displays based on simulating transfer functions of sound paths from sound sources to listeners' external ears, and a sound field simulator based on precise sound field analysis and control are two examples. These systems are expected to provide a high-quality 3D virtual sound space, which is keenly required to realize in the multimedia communications, cyberspace systems and supere-definition audio-visual display systems. Moreover, in 2008, we put a lot of efforts to investigate the spatiotemporal integration process of audio-visual and audio-vestibular information.

Furthermore, in 2008, we developed new signal processing algorithms of digital watermarking, advanced digital hearing aids, and high-performance two-stage binaural speech enhancement.

[Staff]
Professor: SUZUKI, Yôiti Dr.
Associate Professor: IWAYA, Yukio Dr.
Assistant Professor: SAKAMOTO, Shuichi Dr., and MIYAUCHI, Ryota Dr.
Technical Official: SAITO, Fumitaka
Research Fellow: KOBAYASHI, Maori Dr., TERAMOTO, Wataru Dr., and OTANI Makoto Dr.

[Papers]
- Satoshi Yairi, Yukio Iwaya and Yôiti Suzuki: “Influence of large system latency of virtual auditory display on behavior of head movement in sound localization task,” Acta Acustica united with Acustica, 94 (6), 1016-1023, 2008

Fig. 1 Application for training spatial cognition based on high-definition virtual auditory display
Visual Cognition and Systems

Vision sciences for visual communication

[Research Target and Activities]
In order to realize human oriented information communication systems and visual environments gentle to humans, it is necessary to understand the functions of the human visual system. The aims of visual cognition and systems laboratories are to investigate on vision functions in the human brain and to apply the knowledge to human factors and image sciences.

1. Parallel motion mechanisms
We have been studying on the two types of motion detectors using motion aftereffect with different spatiotemporal properties. This year we published an article that made clearly that sensitivity of the slow and fast motion detectors are different in spatiotemporal frequency, stimulus orientation, but they are not different in the effect of attention and speed. We also found that motion detecting system in vision is related to haptic movements. This could impact the field of vision science with some more detailed investigation since this suggests that the visual process does not simply process visual information.

2. Binocular integration of motion and color signals
There are two binocular process for perception of motion in depth. We revealed that these two processes are different in terms of temporal property. The motion-in-depth process that uses interocular velocity differences is sensitive to relatively higher temporal frequency than the one that uses disparity change in time. This indicates that these two process work complementary: one detects high speed motion and the other detects low speed motion. We also revealed that there is a binocular color process that integrates color information from the two eyes when the colors from the two eyes are similar (binocular AND system).

[Staff]
Professor : SHIOIRI, Satoshi Dr.
Associate Professor : KURIKI, Ichiro Dr.
Assistant Professor : MATSUMIYA, Kazumichi Dr.

[Profile of professor SHIOIRI]
Professor Shioiri graduated Tokyo Institute of Technology and received Dr. Eng in 1986. He was a postdoctoral researcher at University of Montreal from 1986 to 1989. From June of 1989 to April of 1990, he was a research fellow at Auditory and Visual Perception Laboratories of Advanced Telecommunications Research Institute. He moved to Chiba University at May of 1990, where he spent 15 years as an assistant professor, an associate professor, and a professor of Department of Image Sciences Department of Image, Information Sciences and Department of Medical Systems. In 2005, he moved to Tohoku University. Since then, he has been a professor of Research Institute of Electrical Communication of Tohoku University. His research interests cover early, middle and high level vision, including motion perception, depth perception, color vision, visual attention, eye movements, modeling of visual functions and comparison of the conscious and unconscious vision processes.


Contact to Professor Satoshi SHIOIRI : shioiri@riec.tohoku.ac.jp
Interdisciplinary field for informatics
Fabrication and Application of Plasmonic Ag Nanosheet

[Research Target and Activities]
The interdisciplinary field of research combining nanobiotechnology and information technology attracts our exceeding interests. These studies stand on the development of new materials created by self-assembly of functional molecules and nanomaterials, leading to new classes of biosensing devices. The plasmons, especially the combination of surface plasmons propagating along the metal-organic interface and local surface plasmons on metal nanoparticles is crucial to control and manipulate localized light in nanoscale for biosensing.

Fig.1 shows our latest research subject concerning "Plasmonic Ag nanosheet". Uniformly sized Ag nanoparticles (d = 5 nm) form a homogeneous monolayer composed of two-dimensional (2D) crystalline domains at air-water interface by self-assembly, in which the distance between Ag cores is accurately controlled by the thickness of the capping organic molecules. The nanosheet exhibits a significant red-shift of plasmon absorption band from the position in dispersion solution, while the peak width was rather reduced (sharpened) as an evidence of homogeneous coupling of local plasmons. This flexible, transferable nanosheet, which can trap and transport bulk light at nano-interface, promises new application in the field of bio- and organic devices.

Reversible work function change of gold electrode by photoisomerization of azobenzene self-assembled monolayers has been investigated by UPS in Singapore synchrotron facilities as well.

[Staff]
Professor: TAMADA Kaoru Dr. (from Oct. 2007)

[Profile of Professor TAMADA]

[Papers]

Contact to Professor Kaoru TAMADA : tamada@riec.tohoku.ac.jp
Ubiquitous Communications system
Research and Development on Super Broadband Wireless Communications

[Research Target and Activities]

(Aims)
To realize communications environments in which everybody can communicate with anybody, anywhere and anytime by carrying out researches from radio propagation characteristics to radio equipment with which customers can communicate universally.

To contribute to materialize or to lead IEEE standardization based on “Japanese technologies”

(Achievements)
To realize Multi-Gigabit per second transmission by employing millimeter wave (60GHz) indoor WPAN (Wireless Personal Area Networks) systems, a lot of measurements and experiments have been done on antenna directivity / beam width, delay spreads, reflective waves by walls and metals (or equivalents) and they have lead to the inventions of simultaneous multiple signal path tracking (including the intentionally created paths) and dynamic path selection to enhance communications reliability. Also, a lot of physical layers have been studied, designed and simulated to develop robust WPAN systems in various and practical fading environments. Moreover, ultra high speed MAC (Medium Access Control) has been designed and developed for high throughput multi-Gigabit WPAN.

Through organizing a Consortium (COMPA, Chair: Professor Shuzo Kato) (22 active members) and getting a group of supporting COMPA as Partners (17 non-Japanese institutes), a global and harmonized IEEE standardization (IEEE802.15.3c) has been executed successfully (the standardization has been finalized in September 2009). This is the first successful standardization of “Japanese organization driven IEEE standardization and it will a good role model of IEEE standardization for Japanese industry.

[Staff]
Professor Shuzo Kato, Ph.D.
Associate Professor Hiroyuki Nakase, Ph.D.

[Profile of Professor Shuzo Kato]
Prof. Shuzo Kato received his Ph. D degree in electrical and communications engineering from Tohoku University, Sendai Japan in 1977. From 1977 to 1995, he worked at NTT (Nippon Telegraph and Telephone) Research Laboratories in Japan, specializing personal and satellite communications systems R&D. He founded Pacific Communications Research Corp. focusing on ASIC, SW and system design for PCS In 1995, at the same time he served as Senior Executive Vice President, and later as President of Uniden Corporation. From January 1999 to July 2001, he served as Executive Vice President, Mitsubishi Wireless Communications Inc (MWCI) in USA, as well as President, Mobile Communications Technology Center of MWCI in San Diego, CA responsible for mobile phone technology development up to real/sellable and high yield cell phones with all certificates (FCC, CTIA and inter-operability). From 2002 to 2005, he served as Executive Vice President of Teradyne Japan responsible for P/L, Engineering, Production and Global Marketing as well as President and CEO of Omni Wireless Inc., in California, USA.

He currently is Professor, Research Institute of Electrical Communications, Tohoku University, Japan, Program Coordinator, Ubiquitous Mobile Communications at NICT (National Institute of Information and Communications Technology) working on wireless communications systems R&D focusing on millimeter wave communications systems. He has been serving as Vice-chair of IEEE802.15.3c Task Group working on millimeter wave systems standardization and Chair of COMPA (Consortium of Millimeter Wave Systems Practical Applications) promoting millimeter wave systems globally. He has published over 200 technical papers, held over 75 patents (including a patent which became DOD (Department of Defense, USA) standard in 1998), co-founded International Symposium on Personal Indoor and Mobile Radio Communications (PIMRC). He is a Fellow of the IEEE and IEICE Japan and served as an Editor of IEEE Transaction on Communications, Chairman of Satellite and Space Communications Committee, COMSOC IEEE, a Board Member of IEICE Japan.

[Papers]

Contact to Professor Shuzo KATO : shukato@riec.tohoku.ac.jp
Research Targets and Activities of Systems & Software Division

The goal of System & Software Division is to realize the Ubiquitous environment. In an ideal ubiquitous environment, everyone can communicate with anybody, anywhere, at any time, with any kind of information, freely and in real time. Our division has the following 5 research fields related to such high-level system, software and contents by integrating computer and communication:

- Software Construction: Reliable and high-level software.
- Communication Network Systems: Symbiotic computing.
- Information Content: Network-oriented contents management and utilization technology.
- Structure of Information Society (Visitor Section).

Research results from 2007 Apr. to 2008 Mar. in each field are described later. The summary is as follows.

(1) Software Construction

We have been researching on theoretical foundations for flexible yet reliable programming languages, and have been conducting a development project of SML#, a new programming language in the ML family, that embodies some of our recent results such as more flexible static typing and high-degree of interoperability with existing languages and databases. The last year's major results include the following. (1) Development of a new proof system based on the proof theory for machine code we developed in the previous year. This proof system yields a term calculus suitable for compiler intermediate representations and a compilation algorithm that deals with control flow merge without resorting to ad-hoc encoding. (2) Development of the SML# Compiler. In this year, we have developed a prototype code generator and a new runtime system.

(2) Computing Information Theory

Aiming at combining program transformation methods and automated theorem proving methods, we continued to pursue the possibility of program transformation by second-order templates based on term rewriting. We proposed a second-order generalization algorithm and based on this, we implemented a method to extract second-order templates from concrete program transformations. Although many automated termination provers have been proposed recently, little work is reported on automated confluence provers. We developed an automated confluence prover ACP for term rewriting systems based on several divide-and-conquer methods such as direct sum decomposition and commutative decomposition. Other research results of this year include a new automated lemma.
generation method for rewriting induction with disproof and a new termination proof for higher-order rewriting systems with recursive path relations. 

(3) Communication Network Systems

We have studied new management schemes for next generation ubiquitous networks (SCOPE project, 2007-2009, Ministry of Internal Affairs and Communications) and promoted standardization activities in IETF WG. As an outcome of this work, our proposed idea of “NEMO Management Information Base” becomes the final draft standard in January 2009. It will be an official Internet standard (RFC) in a couple of months. In the field of “Symbiotic computing”, we have investigated the details of its model and architecture, and have developed some prototype systems, such as supervisory support system for elderly people (uEyes). We were invited for four keynote speeches and one invited paper in well-known international conferences. We have also received a Best Paper Award in a domestic workshop. Moreover, we also issued a press release on real/cyber space integration experiment in July 2008 that had attracted wide press coverage.

(4) Information Content

We have been developing creation technologies, retrieval technologies and distribution technologies for information content. We are developing DMD(Digital Movie Director), which makes it possible to generate 3DCG animation content only by text (scenario) input, and AR (augmented reality)-based cloth fitting technology etc about creation technologies. As retrieval technologies, we are studying a high-performance video shot boundary detection technology, similar image retrieval technology based on PCA-SIFT and so forth. Besides, we are proposing a new cyclic broadcasting method and an overlay multicast technology for low-cost & high-performance video distribution.

(5) Structure of Information Society

“Numerical simulation technologies by ultra high performance computing” is a powerful measure to encourage human innovative activities in the human society. Our research focuses on the basic technologies to make it spread across the world. We have obtained a basic design of special purpose processor to accelerate the speed of fluid simulation which can be widely applicable to the various fields. The computing is expected to be done with ultra high speed and with low power consumption by using compact-size electric circuits. We have adopted a simple algorithm of lattice gas automaton and a systolic array of operation circuits. In addition, we have bright perspective to attain much lower power consumption by using “spin operation circuits with TMR devices” proposed by Professor Hanyu of RIEC.
Software Construction
Foundations for Developing High-level and Reliable Programming Languages

[Research Target and Activities]

Today's software systems are becoming more and more complicated due to the need of integrating various computation resources available in the Internet. A key to control the complexity and to enhance the reliability of such a system is to develop a high-level programming language that can directly represent various resources and automatically detect potential inconsistencies among the components in a system. Based on this general observation, our research aims at establishing both firm theoretical basis and implementation method for flexible yet reliable programming languages for advanced applications. Research topics on theoretical foundations include: logical foundations for compilation, verification of low-level code, and type-directed compilation for polymorphic languages. We are also developing a new practical ML-style programming language, SML#, which embodies some of our recent results such as record polymorphism, rank-1 polymorphism, and high degree of interoperability with existing languages and databases.

The major results of 2008 academic year include the following. (1) Development of a new calculus for representing control flow merge. In existing formalisms such as Λ-normal forms or CPS terms, control flow merge is treated in an ad-hoc way by introducing extra beta-redex. This seems to be due to the lack of proper formalism for representing case expression in the underlying calculi. In this work, we refine the correspondence between the sequent calculus and Λ-normal forms shown by one of the authors, and develop a proof system where control flow merge is directly represented as a logical rule for disjunction. These results yield a term calculus suitable for compiler intermediate representations and a compilation algorithm that deals with control flow merge without resorting to ad-hoc encoding. These results scale up to intermediate languages for practical compilers. The proposed calculus has been successfully used as an intermediate language of the SML# compiler. (2) Development of the SML# Compiler. In this year, we have developed a prototype code generator and a new runtime system.

[Staff]
Professor: OHORI Atsushi Dr. (2005-)
Assistant Professor: SASANO Isao Dr. (2005-2008)
Secretary: OTOMO Shoko

[Profile of Professor OHORI]
Professor Atsushi Ohori was born in 1957. He received his BA degree in Philosophy from University of Tokyo, 1981; received his MSE degree in Computer and Information Science from University of Pennsylvania, 1986; and received his Ph.D. degree in Computer and Information Science from University of Pennsylvania, 1989. He worked for Oki Electric Industry as a programmer, a researcher and a senior researcher from 1981 until 1993. From 1989 until 1990, he spent one year in University of Glasgow as a postdoctoral research fellow funded by Royal Society Research Fellowship. In 1993, he joined Research Institute for Mathematical Sciences, Kyoto University as an Associate Professor. In 2000, he joined Japan Advanced Institute of Science and Technology as a Professor. In 2005, he moved to RIEC, Tohoku University as a Professor.

[Papers]
Computing Information Theory
Towards a New Software Paradigm Arising from Computation and Proof

[Research Target and Activities]
We are working on the development of a new software paradigm that arises from computation and proof. For this, we focus on a rewriting formalism which offers both flexible and effective reasoning with equations. In the rewriting formalism, proofs by equational reasoning and computations by rewriting systems can be combined in a unified framework (see the figure above). We aim at applying our new paradigm to the development of formal techniques for construction and verification of reliable software. We are currently working on rewriting theories for termination, confluence, program transformation, and program verification. Recent research activities include higher-order rewriting systems, automated inductive theorem proving, combination of functional-logic languages and automated theorem proving systems.

[Staff]
Professor : TOYAMA, Yoshihito Dr
Associate Professor : AOTO, Takahito Dr
Assistant Professor : KIKUCHI, Kentaro Dr
Research Fellow : KETEMA, Jeroen Dr

[Profile of Professor TOYAMA]
Yoshihito Toyama was born in 1952. He received his B.E. from Niigata University in 1975, and his M.E. and D.E. from Tohoku University in 1977 and 1990. He worked as a Research Scientist at NTT Laboratories from 1977 to 1993, and as a Professor at the Japan Advanced Institute of Science and Technology (JAIST) from 1993 to 2000. Since April 2000, he has been a professor at the Research Institute of Electrical Communication (RIEC) of Tohoku University. His research interests include term rewriting systems, program theory, and automated theorem proving.

[Papers]
Communication Network Systems

Information Communication Systems based on Symbiotic Computing

[Research Target and Activities]
Aiming towards the next generation ubiquitous stage we are pursuing a research on information and communication paradigm, called “Symbiotic computing”. The purpose of this work is to establish a method comprising of flexible information and telecommunication system with the co-existence of human and IT environment. This computing paradigm operates with stability while satisfying user and system provider’s criteria. We have been promoting both theoretical and practical researches of Symbiotic computing based on the concept of flexible information network where human and IT environment co-exist. Our focus also includes performance evaluation of super-high-speed network and its efficient management.

(1) Symbiotic computing: Theory and application.
(2) Symbiotic society and health-care/watch-over support/real/cyber space integration
(3) Measurement/analysis of super-high-speed wide-area networks, and mobile network management.

In 2008, for Symbiotic computing research, we have studied model and architecture of Symbiotic computing in detail. We also issued a press release on real/cyber space integration experiment in Jul. 2008. For network management research, we have investigated IPv6 network mobility support and designed a MIB specialized in network mobility. We also promoted standardization activities in IETF Working Group and proposed the final draft standard of “NEMO Management Information Base” in Jan. 2009. This draft will be an official internet standard (RFC) in a couple of months.

[Staff]
Professor: SHIRATORI, Norio Dr.
Associate Professor: SUGANUMA, Takuo Dr.
Research Associate: KOIDE, Kazuhide Dr. (until Jan. 2008)
Secretary: MIURA Kana, MORIYA Kaori

[Profile of Professor SHIRATORI]
Prof. Shiratori was born in 1946 in Miyagi Prefecture. He received his doctoral degree from Tohoku University in 1977. He is currently a Professor at RIEC. Before moving to RIEC in 1993, he was the Professor of Information Engineering at Tohoku University from 1990 to 1993. Prior to that, he served as an Associate Professor and Research Associate at RIEC. He received IEEE Fellow in 1998, IPSJ Fellow in 2000 and IEICE Fellow in 2002. He is the recipient of many awards including, IPSJ Memorial Prize Wining Paper Award in 1985, IPSJ Best Paper Award in 1996, IPSJ Contribution Award in 2007, IEICE Achievement Award in 2001, IEICE Best Paper Award, IEEE ICOIN-11 Best Paper Award in 1997, IEEE ICOIN-12 Best Paper Award in 1998, IEEE ICPADS Best Paper Award in 2000, IEEE 5-th WMSCI Best Paper Award in 2001, UIC-07 Outstanding Paper Award in 2007, Telecommunication Advancement Foundation Incorporation Award in 1991, Tohoku Bureau of Telecommunications Award in 2002, etc. He was the vice president of IPSJ in 2002, IFIP representative from Japan in 2002, an associate member of Science Council of Japan in 2007. He is working on methodology and technology for symbiosis of human and IT environment.

[Papers]

Contact to Professor Norio SHIRATORI: norio@shiratori.riec.tohoku.ac.jp
Information Contents

Creation, Understanding, Distribution and Consumption of Next-generation Digital Content

[Research Target and Activities]
In our laboratory, we developed new technologies this fiscal year.

1. Automatic Character Positioning Method in DMD
   We developed automatic character positioning method in DMD (Digital Movie Director), which is innovative and useful because user does not have to think of three dimensional CG space.

2. A Cyclic Broadcasting Method for NVoD for IPTV era
   We developed a new broadcast scheduling method for end user to distribute their video content with their PC and network resources. The features of this method is to own network-error resilience.

3. A New Cloth Simulation Method based on Complex Systems
   A light-weight cloth simulation system is thought of as one of the most difficult implementations in CG field. We developed a new cloth simulation algorithm based on the theory of complex systems, the feature of which is a light-weight processing.

4. An Intuitive Character Motion Creation System
   In general, character motion creation is difficult to generate for most of CG creators. We developed a new system based on arrow-input interface. CG creators can generate new motions more and more easily by this system.

5. High-performance Video shot Boundary Detection Technology
   Existing shot boundary detection methods are difficult to detect all of shot boundaries exactly. This is why we proposed a new approach to handle numerous video features based on multivariate statistics. This method showed our proposal to detect most of shot boundaries which existing methods are difficult to detect.

[Staff]
Professor : NUMAZAWA, Junji Dr.
Associate Professor : AOKI, Terumasa Dr.
Secretary : MUTO Nobuko

[Profile of Professor Numazawa, Junji]
Junji Numazawa received the M.E., and Ph.D degrees in electronics engineering from Hokkaido University, Sapporo, Japan, in 1971, and 1994, respectively. He joined NHK (Japan Broadcasting Corp.) in 1971. He was Director of the Science and Technical Research Laboratories of NHK in June 1996, Director of Engineering Administration Department of NHK in June 1999, Executive Research Engineer of Science and Technical Research Laboratories of NHK in June 2002. Since 2004, he has been a Professor at the RIEC and GSIS of Tohoku University. He received “Suzuki Memorial Incentive Awards” and “Fuji Frontier Awards” of The Institute of Image Information and Television Engineers (ITE) in 1980 and 1995 respectively, “Distinguished Invited Paper Awards” of IUMRS-ICAM-93 in 1993, “Person Who Has Rendered Distinguished Service Awards” of Tokyo Metropolitan Government in 2003. Dr. Numazawa is a Fellow of the Institute of Image Information and Television Engineers (ITE).

[Papers]

Research area on the structure of information society

Numerical Simulation Technologies by ultra high performance computing as a measure to encourage human innovative activities

[Research Target and Activities]

“Numerical simulation technologies by ultra high performance computing” is a powerful measure to encourage human innovative activities in the human society. Our research focuses on the basic technologies to make the measure spread across the world.

In 2008, we studied on the design of special processors to accelerate the speed of fluid simulation which can be widely applicable to the various fields such as cooling systems of Fast Breeder Reactors, positioning systems of Autonomous Underwater Vehicles, and so on. The main target of the design is to attain ultra high computing speed, very low power consumption and compact in size. For this purpose we have adopted a very simple simulation model of lattice gas automaton. The model is so simple that the recursive computation based on it does not need the complicated calculation such as multiplication with floating-point representation, and that the power consumption by the relevant electric circuits can be kept low. We have completed a basic design of the Compact Simulator for fluid dynamics.

In addition, we have bright perspective to attain much lower power consumption by using “spin operation circuits with TMR devices” proposed by Professor Hanyu of RIEC.

[Profile of Professor MATSUOKA]

Hiroshi MATSUOKA received his B.E. and M.E. from Tokyo University, Japan, in 1977 and 1979, respectively, and Dr. Eng. from Ibaraki University, Japan, in 1997. From 1979, he worked for Science and Technology Agency of Japanese Government, and transferred to Ministry of Industry and International Trade, Japan Atomic Energy Agency, National Institute of Metal, IAEA (International Atomic Energy Agency), Tohoku University and so on.

[Papers]

No papers in 2008

Contact to Professor Hiroshi MATSUOKA : matsuoka@riec.tohoku.ac.jp
Laboratory for Nanoelectronics and Spintronics

The Laboratory for Nanoelectronics and Spintronics of the Research Institute of Electrical Communication was established in 2004. Its purpose is to develop and establish the science and technology of nanoelectronics and spintronics for information technology, utilizing the facilities installed in the Nanoelectronics-and-Spintronics building having 1300 m² of cleanroom area. The Laboratory for Nanoelectronics and Spintronics consists of four Sections:

Atomically Controlled Processing (Junichi Murota, Professor)
Semiconductor Spintronics (Hideo Ohno, Professor)
Nano-Molecular Devices (Michio Niwano, Professor)
Nano-Spin Memory (Shoji Ikeda, Associate Professor)

These Sections cooperatively carry out the research aimed at establishing nanoelectronics and spintronics, together with the groups of Intelligent Nano-Integration System (Koji Nakajima, Professor), Quantum-optical Information Technology (Keiichi Edamatsu, Professor), and Ultra-Broadband Signal Processing (Taiichi Otsuji, Professor) and with the research groups of the Institute, the Graduate School of Engineering, the Graduate School of Information Science, Tohoku University, and nation-wide cooperative research projects in the field.
Highlights of Research Activities in 2008:

**Atomically Controlled Processing [Murota Laboratory]**
In order to create atomically controlled processing for nanometer-order artificial heterostructures of group IV semiconductors, following experimental results have been obtained: (1) In the research on atomic-layer doping with high carrier concentration, low-temperature SiH₄ exposure to suppress B clustering in thermal CVD is effective to improve electrical activity and to suppress lattice strain in Si. (2) By lowering process temperature utilizing plasma CVD without substrate heating, B atomic-layer doping in Si epitaxial growth. (3) In the research on formation of local-strain introduced Si₁₋ₓGeₓ/Si nanometer-order heterostructure, by control of thermal nitridation and Si₁₋ₓGeₓ film formation using low-temperature thermal CVD, N atomic-layer doping in Si₁₋ₓGeₓ epitaxial growth can be realized. (4) Using the Ge thin film substrate with strain relaxation enhancement by ion-energy control in plasma CVD, formed by plasma CVD, nanometer-order thick highly strained Si can be epitaxially grown.

**Semiconductor Spintronics [Ohno Laboratory]**
Our research activities focus on the establishment of the fundamental technologies for future spintronic devices. The outcomes in the last fiscal year are following. (1) We demonstrated the modulation of magnetic anisotropy in a (Ga,Mn)As thin film by purely electrical means, e.g., by the application of electric-fields. (2) We demonstrated phase coherence control of spin-3/2 ⁷⁵As nuclei with multipulse NMR sequences in a GaAs/AlGaAs quantum well by optically-detected nuclear magnetic resonance (NMR). (3) We demonstrated the operation of terahertz (THz) GaAs quantum cascade lasers emitting at 3.8 THz. We also observed intersubband optical transitions in ZnO-based quantum wells for the first time.

**Nano-Molecular Devices [Niwano Laboratory]**
① We investigated the dependence of anodization process of an aluminum microwire on its cross-sectional structure. The anodization process of an aluminum microwire depended strongly on the aspect ratio of the cross-sectional profile of the aluminum microwire. ② Single electron transistors (SETs) were fabricated by controlling anodization process of the aluminum microwire and we demonstrated that the SET fabricated by anodization process can operate at room temperature. ③ Planar bilayer lipid membranes (BLMs) were formed in microfabricated apertures in Si chips. We have succeeded in preparing BLMs which are stable enough for repetitive solution exchanges and allow current recordings at the single-channel level. ④ We have applied our surface infrared spectroscopic method for in situ real-time monitoring of ATP synthesis and hydrolysis in mitochondria extracted for rat livers.

**Intelligent Nano-Integration System [Nakajima Laboratory]**
(1) By using high-order synapses for an inverse function delayed neural network, we
demonstrated that an optimal solution for the traveling salesman problem is obtained as a stationary state similar to the N-Queen problems. Furthermore, we have also proposed a new analysis for nonlinear dynamics, in which a new concept based on the relationship between a potential and an active region is introduced. (2) We evaluated the quality factor of a Bi-2212 intrinsic Josephson junction from its resonant activation property, and found the required condition for observing Rabi oscillation. We studied numerically the computational power of our neuromorphic quantum computation algorithm as a function of the magnitude of decoherence. The result shows that the computational power decreases in proportion to decoherence, but the degradation is smaller than other quantum algorithms. (3) A 4-bit parallel multiplier using a carry look-ahead adder was demonstrated successfully with Nb integrated circuits to improve the performance of high-speed operation for the single flux-quantum fast Fourier transform. A 4-bit high-speed up/down counter for the neural computation using stochastic logic was fabricated and successfully demonstrated.

Quantum-Optical Information Technology [Edamatsu Laboratory]

1. Investigation of optical nonlinearities at a single-photon level is essential in realizing quantum info-communication technology. We have succeeded in measuring the optical Kerr nonlinearity at the single-photon level in a photonic crystal fiber.

2. We are developing a quantum media converter from a single photon to a single electron spin to realize a quantum repeater, which is expected to extend the transmission distance of quantum info-communication. We have achieved optical spin state tomography of optically injected electrons in a semiconductor.

Nanospin Memory [Ikeda Laboratory]

We are developing technologies to realize advanced spin memory and logic devices using magnetic tunnel junctions (MTJs) consisting of ferromagnetic metal electrodes and insulating barriers. In our group, the following results were obtained: 1) The TMR ratios of 604% at RT and 1144% at 5K (world record) were realized in MgO-based MTJs. 2) The use of SyF free layer consisting of Fe-rich CoFeB ferromagnetic layers resulted in low intrinsic critical current density (Jc0) without degrading the thermal-stability factor $\mathcal{E}k_BT$. 3) We developed fabrication process of magnetic tunnel junction (MTJ) on circuits based on 0.14μm node CMOS technology. 4) Operations of the world's first nonvolatile full adder based on logic-in-memory architecture, standby-power-free compact ternary content-addressable memory, and reconfigurable logic block employing the spin-transfer torque writing method were verified.

Ultra-Broadband Signal Processing [Otsuji Laboratory]

The goal of our research is to explore the terahertz frequency range by means of novel electron devices and systems. Graphene has mass-less carriers and their peculiar characteristics are expected to be useful as a candidate to realize such systems. Using an epitaxial graphene formed on silicon substrates provided by Prof. Suemitsu’s group, we have
fabricated a backgate transistor and confirm the drain current modulation by a gate bias voltage. We have also developed a terahertz spectroscopy system using 2D-plasmon-resonant terahertz-wave emitters based on compound semiconductor heterostructures.

**Basic Technology for Broadband Communication [Mizuno Laboratory]**

In this section the development of passive millimeter wave imaging systems is being carried on. In 2008, millimeter-wave optics for 35 GHz-band imaging system have successfully been studied to obtain useful data including depth of field for practical systems. A 35 GHz-band 7 x7 imaging array for getting millimeter wave movie with 2 frames/sec has also been developed.

**Wireless Info Tech [Tsubouchi Laboratory]**

For realizing radio frequency (RF) filter and oscillator of 5-GHz-band broadband wireless terminal, we have developed bulk acoustic wave (BAW) device. Film AlN was manufactured using metalorganic chemical vapor deposition (MO-CVD) method. Using the manufactured AlN, film bulk acoustic resonator (FBAR) was developed. We have evaluated characteristics of FBAR with computer simulation.

**Electromagnetic Bioinformation Engineering [Ishiyama Laboratory]**

We study about a high accuracy strain sensor for detecting the bio-information. In the year 2008, the research work about the internal residual stress of the magnetic materials for this sensor was carried out to obtain the high accuracy. As the result, the experimentally obtained accuracy was one-order higher than the high accuracy semi-conductor strain gage. By using the technique for controlling the magnetic anisotropy as we already established in the magnetic field sensor, the accuracy of this sensor will be improved.

**Solid State Physics [Takahashi Laboratory]**

For high areal density HDD (Hard Disk Drive) over 1 Tbit/inch², achievement of further low RA product, high magnetoresistance ratio and stable magnetization of pinned layer in tunnel magnetoresistance devices are required. Regarding the former requirement, RA product of 2.1 Ωµm² and TMR ratio of 210 % were successfully obtained by employing a novel fabrication process to promote a MgO tunnel barrier crystallization. And inverse TMR effect could be enhanced using epitaxial Cu under layer. Regarding the latter requirement, ferromagnetic component which has exchange coupling with antiferromagnet was observed using PEEM technique as a preliminary study on the design for enhancement of exchange anisotropy. As a result, correlation between exchange filed and domain structure of ferromagnetic layer in Mn-Ir / Co-Fe bilayers are experimentally clarified.

**Electromagnetic Theory [Yamaguchi Laboratory]**

Ni-Fe thin films and Fe particle assembly have been investigated as seeds of new materials
with high performance in the RF(GHz range) applications. The initial permeability measurements of Fe composite particles suggests that, with the help of Fe$_3$O$_4$ nanoparticle (10 nm), Fe particles (1 μm) is almost free from the demagnetizing field, resulting in enhancement of Fe permeability up to its intrinsic values. This effect was exemplified more significantly with the particle composites that include Ni$_{50}$Fe$_{50}$ or Fe-Si-B-Cr particles. According to a calculation of the eddy current in spheres with high permeability, reduction of the particle size to less than 1 μm is essential for further improvement of the high-frequency performance of particle composites.

As for Ni-Fe films, the damping constant(α) of various thick Ni-Fe films was estimated using a coplanar waveguide (CPW)-FMR system. It was found that α is almost maintained constant for film thickness in the range of 10 - 1000 nm. The value of α was 0.012-0.014, which is larger than that of bulk-Ni-Fe.

**Microelectronics [Sahashi Laboratory]**

It is important to understand the mechanism of microwave generated by spin transfer torque which relates to the damping parameter (α) of ferromagnetic material. Experimentally, in this study, a appropriate electrode structures for the measurement of α were investigated. For the measurement of spin dynamics generated by spin transfer torque, we designed several masks of coplanar wave guide (CPW) electrode structure which the high frequency can transmit. The CPW transmission line and attenuation characteristics are evaluated by S11 parameter of network analyzer. The CPW transmission line is found to show good noise attenuation characteristics. The signal reflection is relatively small, being below −5 dB. The magnitude of the reflected signal increases progressively with increasing cross section area. The study on spin torque oscillation and measurement of α for perpendicular magnetized thin films will be continued by using the most suitable pattern of electrode for microwave transmission.

**Solid State Electronics [Itoh Laboratory]**

High-performance TFTs with pseudo-single-crystal silicon has been investigated. The effects of a cap SiO$_2$ at the laser crystallization were investigated. The cap SiO$_2$ suppressed the generation of void in silicon thin film, and the available condition of the lateral crystallization was expanded. By using the cap SiO$_2$ thin film, the surface of the laser-crystallized poly-Si thin film became smooth. The chemical mechanical polishing (CMP) on laser-crystallized poly-Si thin films was carried out. After CMP with conventional colloidal silica slurry, the surface of laser-crystallized poly-Si thin films became rougher than that of before CMP. Carbon segregation in the grain boundary was occurred by the laser crystallization. For the improvement of this problem, a hydrophilic organic solution was added to conventional slurry. As a hydrophilic organic solution ethyl alcohol was used, and planarized surface was obtained. The nanograting MOSFET has been investigated to achieve higher current drivability. The mobility difference between the nanograting nMOSFET and pMOSFET became slighter, and
the area balance of the CMOS circuit could be improved.

**Basic Plasma Engineering [Hatakeyama Laboratory]**

Unique optical properties of free-standing single-walled carbon nanotubes (SWNTs) grown by plasma chemical vapor deposition have been investigated. It was found that the photoluminescence intensity of SWNTs drastically increases through the morphology transition from completely-isolated to thin-bundled structure. Optoelectrical measurements have been realized for fullerene (C_{60}) and azafullerene (C_{59}N) encapsulated SWNTs. The threshold gate voltage to turn on the SWNTs device shifted after the light irradiation. Based on the investigation of electron donor and acceptor encapsulated SWNTs, the dopant combination was found to be a critical factor to decide p-n junction features. The hump current feature can be observed only when a symmetrical depletion layer is formed between a p-n junction area.

**Optical Physics Engineering [Yamada Laboratory]**

The silicon photonic-wire waveguides, with silicon (Si) core and silica (SiO_2) cladding, have achieved light confinement in nanometer size small cross-section waveguides. Therefore, compared with all-silica devices, a huge reduction in size and power consumption is expected. We attempt to fabricate ultra-small Si photonic-wire waveguide devices using electron beam lithography and inductively coupled plasma dry etching techniques. We achieved the fabrication of in-line Bragg gratings with 400 nm cycle and 75 nm air gap, and the directional couplers with 120 nm gap between waveguides based on 300 × 300 nm cross-section Si wire waveguides.

**New Intelligence for IC Differentiation Project [Ohmi Laboratory at New Industry Creation Hatchery Center]**

Current drivability improvement of the p-channel MOS transistor is necessary for the performance enhancement of the CMOS circuit. However, the p+ region of the p-channel MOS transistor has a problem that the boron is easy to be inactivated by plasma damages. Therefore, the increase of the resistance in the p+ region becomes limit the current drivability of the p-channel MOS transistor. We realize that the formation of the high-quality silicon dielectric films at low temperature (<400°C). Those films have approached the electrical characteristics of high temperature thermal silicon dielectric films. The low resistivity (<10 Ω·cm) silicon/silicide contact processes are indispensable to the CMOS circuit fabrications without plasma damages for the p+ region.

**Spin Electronics [Ando Laboratory at Department of Applied Physics]**

Magnetic tunnel junctions (MTJs) with a half-metallic Co_2MnSi Heusler alloy electrode and a high-quality MgO tunneling barrier have been developed. We have succeeded to observe a very large tunnel magneto resistance (TMR) ratio of 217% at RT and 753% at 2K. The
observed large TMR ratio results from both half-metallicity of Co₂MnSi and coherent tunneling through the crystalline MgO barrier. In addition, we succeeded to suppress temperature and bias voltage dependence of TMR ratio by inserting a very thin CoFeB layer into the Co₂MnSi/MgO interface.

**Superstructured Thin Film Chemistry [Kawasaki Laboratory at Institute for Materials Research]**

One of oxide heterointerfaces, MgZnO/ZnO, can provide an interesting arena for investigation of quantum transport properties with high-mobility two-dimensional electron gas (2DEG). We have realized to control the 2DEG density by electrostatic field effect with polymer poly(3,4-ethylenedioxythiophene) poly(styrenesulfonate) (PEDOT:PSS) Schottky contact or MIS structure. In fact, we observed metal-insulator transition at zero magnetic field and systematic variation of quantum Hall states as a function of the 2DEG density under applied magnetic field. The improved controllability will lead to higher performance of transparent field effect transistor.

Co-doped TiO₂ is one of room temperature ferromagnetic oxide semiconductor. In this study, we fabricated Co-doped TiO₂ films on glass substrates by sputtering method. The films exhibited ferromagnetic MCD and also anomalous Hall effect at room temperature. In addition, we observed the large enhancement of magneto-optical effect in a simple one-dimensional magnetophotonic crystal structure.

**Materials Quantum Science [Nitta Laboratory at Department of Materials Science]**

We have investigated the spin orbit interaction (SOI) in semiconductor heterostructures. (1) We demonstrated gate voltage control of spin precession in InGaAs mesoscopic ring arrays with Al₂O₃ gate insulator. Al’tshuler–Aronov–Spivak oscillations were clearly observed and the oscillation phases at B = 0 switched between negative and positive by changing the gate bias voltages. It corresponds to the electrical manifestation of spin rotation due to the Rashba SOI. (2) We proposed a method to determine the relative strength of Rashba and Dresselhaus spin-orbit interaction from transport measurements without the need of fitting parameters. In narrow quantum wires which exhibit weak localization even in the presence of spin-orbit coupling, an in-plane magnetic field can suppress the weak localization effect. We employed the unique angular dependence of this effect to suggest a method for the direct and experimental determination of the ratio between Rashba and Dresselhaus spin-orbit strengths from transport measurements.

**Nanoscale magnetism and devices [Kitakami Laboratory at Institute of Multidisciplinary Research for Advanced Materials]**

The current research subjects in the research group are development of high-Kₗ materials and investigation of magnetization reversal behavior of nanoscale magnets, aiming to development of ultra-high density non-volatile magnetic memory. One of the recent topics is
study on magnetization reversal behavior of nanoscale hard magnetic material under a short pulse field. We have developed a nanoseconds pulse generator with the maximum field of 3 kOe and irreversible magnetization behavior of a nanoscale Co/Pt multilayer dot was investigated.

Solid State Photophysics Group [Ishihara Laboratory at Department of Physics]

In our laboratory, artificial periodic structures (metamaterials and photonic crystals) at visible and near-infrared wavelengths have been investigated. Based on the transmission line metamaterials at microwaves, we designed a visible metamaterial, fabricated it, and evaluated the optical characteristics and the focusing effect. Besides plasmonic crystals and plasmonic lens were fabricated and measured at visible wavelengths. We found novel behavior of eigen modes such as pair annihilation, extreme enhancement of transmission, and plasmonic resonant second harmonic generation.

Dept. of Biomedical Engineering [Tanaka Lab at Graduate School of Biomedical Engineering]

The novel SPRAM-based reconfigurable logic block has been proposed, and fabricated through 140nm CMOS process technology combined with a MTJ process. This logic block included the SRAM for LUT and the SPRAM for configuration data, where both memories were embedded in one chip. The MTJ element with the area size of 50x200nm2 and CMOS logic circuit were fully integrated. Experimental results showed that the reconfigurable logic block achieved 25MHz read-out operations with the magnetic resistance of 1.62kohm (parallel) and the MR ratio of 91.7%.

New Paradigm VLSI System [Hanyuu Laboratory]

Our research group has fabricated a nonvolatile logic-in-memory integrated circuit, where a nonvolatile storage function is merged into a logic-circuit function, using magnetic tunnel junctions (MTJs) in combination with MOS transistors. It is the first time to succeed the fabrication of this type of integrated circuit, where the MOS-transistor part is fabricated by Hitachi Co., Ltd. and the MTJ-device part is fabricated by Laboratory for Nanoelectronics and Spintronics, Research Institute of Electrical Communication, Tohoku University. This is the collaboration of Prof. H. Ohno’s, Prof. S. Ikeda’s and our laboratories.

Low Temperature Condensed State Group [Iwasa Lab at Institute for Material Research]

Very thin single crystals fabricated by mechanical micro cleavage are materials received a great interest recently as promising candidates for microelectronics due their unique electronic properties. Our research is based on the combination of layered materials and electrochemical transistors for the advantages as large tunable electron density at lower gate voltage. Layered-material EDLTs are successfully fabricated on the top of SiO2/Si substrates. The thickness of the layered material samples, single, double or multi-layers, is
characterized by Raman scattering and atomic force microscope. The electronic devices are fabricated by standard photolithography and e-beam lithography method by using the facilities in Laboratory for Nanoelectronics and Spintronics. By applying ionic liquid as the gating media, we observed ambipolar characteristics by low gate voltage smaller than 1 V on graphene EDLT. As a general method, we can apply it to a broader spectrum of material.

**Technology Development Division Storage Technology Group [Fujimoto Laboratory]**

A cooperative research project between industry, academia and government: Development of super high-speed mass storage HDD systems started in August 2007 under the collaborations between RIEC including IT21 storage technology group, major Japanese HDD manufacturers and other laboratories researching related technologies within Tohoku University. Developments of high density patterned media and high sensitivity sensor are research subjects in this project, and we fabricated fine dot arrays of Co based alloy films and spin accumulation devices and studied fundamental properties.

**Solid State Electronics [Suemitu Laboratory]**

Graphene is expected to be a key material in the next-generation semiconductor devices. The main challenge for the graphene devices, however, is the lack of cost-effective fabrication method of graphene with large scale. To solve this point, we have been studying formation of graphene on silicon substrates (GOS) by use of heteroepitaxy of SiC on Si. As a result, we have succeeded in realization of a GOS structure for the first time. This achievement allows us to form graphene on low-cost, large-scale silicon wafers, which will lead to graphene-based electronics in the future.

**Researching Section [Endoh Lab at Center for Interdisciplinary Research]**

To develop a novel Spin device technology for the Magnetic Tunneling Junction (MTJ) combined with the Si-CMOS circuit, we have systematically studied the Spin device technology, the integration process technology, the circuit design and the measurement. To realize the ultra high-speed operation of MTJ, we have proposed the new circuit technique of the Dynamic Feedback MOS Current Mode Logic (DF-MCML) and the Current Controlled MCML (CC-MCML), which function low power consumption in the GHz operation. This circuit technique has realized the stable operation even in the presence of threshold voltage fluctuations by using the feedback technology. We have successfully fabricated the DF-MCML and the CC-MCML inverter with the 180nm CMOS process, for the first time.
Atomically Controlled Processing

Creation of Atomically Controlled Processing of Group IV Semiconductor and Application to Nano Heterodevices

[Research Target and Activities]

Development of atomically controlled processing technology in deposition and etching is quite important to fabricate future higher-performance ultralarge-scale integrated circuits (ULSIs) as well as quantum devices for new functions and to create new materials with novel properties which are different from that of conventional bulk materials. To overcome the limits of Si material properties and device miniaturization and to achieve on-chip integration of ultimate charge control into Si ULSIs, this laboratory aims to establish atomically controlled processing for nanometer-order artificial heterostructures of group IV semiconductors with atomically controlled surface and interfaces and nanometer-order three-dimensional patterning with molecular control to fabricate nanometer-order heterostructure devices. (Fig. 1)

In this year, following experimental results have been obtained: (1) In the research on atomic-layer doping with high carrier concentration, low-temperature SiH₄ exposure to suppress B clustering in thermal CVD is effective to improve electrical activity and to suppress lattice strain in Si. (2) By lowering process temperature utilizing plasma CVD without substrate heating, B atomic-layer doping in Si epitaxial growth. (3) In the research on formation of local-strain introduced Si₁₋ₓGex/Si nanometer-order heterostructure, by control of thermal nitridation and Si₁₋ₓGex film formation using low-temperature thermal CVD, N atomic-layer doping in Si₁₋ₓGex epitaxial growth can be realized. (4) Using the Ge thin film substrate with strain relaxation enhancement by ion-energy control in plasma CVD, formed by plasma CVD, nanometer-order thick highly strained Si can be epitaxially grown.

[Staff]

Professor: MUROTA, Junichi Dr.
Associate Prof.: SAKURABA, Masao Dr.

[Profile of Prof. J. Murota]

Prof. J. Murota was born in 1948. He received the B.E., M.E. and Ph.D degrees in electronic engineering from Hokkaido University in 1970, 1972 and 1985, respectively. He joined the Musashino Electrical Communication Laboratory, Nippon Telegraph and Telephone Public Corporation (NTT) in 1972. In 1983, he moved to the Atsugi Electrical Communication Laboratory, NTT. In 1985 he became an Associate Professor in the Research Institute of Electrical Communication, Tohoku University and in 1995 became a Professor. He is actively involved in researches on atomically controlled processing of group IV semiconductors. He was awarded the 3rd (2003) Yamazaki-Teiichi Prize.

[Papers]


Semiconductor Spintronics

Nanoscience and Nanotechnology for Spintronics and THz Lasers

[Research Target and Activities]

We are working on the nanoscience and nanotechnology to control the quantum states in semiconductors, especially the spin states and optical transitions in the mid-infrared to THz.

Materials of interest include GaAs/AlAs, InAs/(Al,Ga)Sb, GaN, and ZnO, with and without doping of magnetic elements, all grown by molecular beam epitaxy. We are investigating electrical, optical, magnetic properties of these materials and their application to new functional devices, such as memories and logic devices using spin states as well as quantum cascade lasers (QCL) with THz emission.

The outcomes in the last fiscal year are (1) Demonstration of electric-field control of magnetic anisotropy and magnetization rotation in a (Ga,Mn)As thin film. (2) Demonstration of the phase coherence control of spin-3/2 75As nuclei with multipulse nuclear magnetic resonance (NMR) sequences in a GaAs/AlGaAs quantum well by optically-detected NMR. (3) Demonstration of a GaAs THz quantum cascade laser emitting at 3.8 THz and first observation of intersubband optical transitions of ZnO quantum wells.

[Staff]
Professor: OHNO Hideo Dr.
Associate Professor: OHNO Yuzo Dr.
Associate Professor: MATSUKURA Fumihiro Dr.
Assistant Professor: OHTANI Keita Dr.

[Profile of Professor OHNO]
Hideo Ohno received Ph. D. degree from the University of Tokyo in 1982. He was with the Faculty of Engineering, Hokkaido University as a Lecturer (1982) and then as an Associate Professor (1983). He moved to Tohoku University in 1994 as a Professor. He received the IBM Japan Science Prize (1998), the IUPAP Magnetism Prize (2003), the Japan Academy Prize (2005), and the 2005 Agilent Technologies Europhysics Prize. He is Institute of Physics (IOP) Fellow (2004), Honorable Professor at Institute of Semiconductors, Chinese Academy of Sciences, and JSAP fellow (2007), Distinguished Professor at Tohoku University (2008), and IEEE Magnetic Society Distinguished Lecturer for 2009. He is a member of JSAP, JPS, JACG, IEICE, APS, IOP, IEEE, and AVS.

[Papers]

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Nano-Molecular Devices
Control of surface and interface of molecular informational devices and development of novel nano-molecular devices

[Research Target and Activities]
Our research aims at application of the Si technology to organic semiconductor devices or many kinds of biosensors. We have investigated development of 1) an in-situ monitoring system of cultured cells or biological materials using infrared absorption spectroscopy, 2) an ion channel sensor using a Si technology, and 3) a nanofabrication method using electrochemical processes such as anodization of valve metals. Especially, we have applied TiO₂ nanotubes formed by anodization to dye-sensitized solar cells (DSSC).

1) An in-situ monitoring system for cultured cells
We have succeeded in discrimination between apoptotic and viable cells using infrared spectroscopy and demonstrated that the cell apoptotic process could be analyzed through protein amide bands.

2) An ion channel sensor using a Si technology
Using a Si microfabrication technology, we have fabricated an artificial lipid bilayer in which gramicidin peptides can form transmembrane ion channels. The lipid bilayer exhibited mechanical strength enough for application to drug screening.

3) A nanofabrication method using electrochemical processes
We demonstrated that a room-temperature operation single electron transistor could be fabricated by controlling a cross-sectional profile of an Al microwire before anodization.

[Staff]
Professor : NIWANO, Michio Dr.
Assistant professor : KIMURA, Yasuo Dr.

[Profile of Professor NIWANO, Michio]
1998 - present RIEC, Tohoku University, Japan, Professor
Memberships: Electrochemical Society (ECS), Material Research Society (MRS), American Vacuum Society (AVS)
Education: March, 1980 Tohoku University, Japan, Doctorate of Science

[Papers]

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Nano-Spin Memory

Research of spin based device and memory

[Research Target and Activities]

We are developing technologies to realize advanced spin memory and logic devices using magnetic tunnel junctions (MTJs) consisting of ferromagnetic metal electrodes and insulating barriers. In our group, the following results were obtained.

1) The TMR ratios of 604% at RT and 1144% at 5K (world record) were realized in MgO-based MTJs.
2) The use of SyF free layer consisting of Fe-rich CoFeB ferromagnetic layers resulted in low intrinsic critical current density ($j_c$) without degrading the thermal-stability factor $E/k_BT$.
3) We developed fabrication process of MTJs on circuits based on 0.14μm node CMOS technology.
4) Operations of the world's first nonvolatile full adder based on logic-in-memory architecture, standby-power-free compact ternary content addressable memory, and reconfigurable logic block employing the spin-transfer torque MTJs with SyF free layer were verified.

[Staff]

Visiting Professor: HASEGAWA Haruhiro Dr.
Associate Professor: IKEDA Shoji Dr.
Research Fellow: Huadong GAN,
Research Fellow: Ji Ho PARK,
Research Fellow: MIURA Katsuya,
Research Fellow: HAYAKAWA Jun,
Research Fellow: YAMAMOTO Hiroyuki,

[Papers]


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Laboratory for Brainware Systems

Research Targets and Activities of Laboratory for Brainware Systems

The Laboratory for Brainware Systems of the Research Institute of Electrical Communication was established in 2004. Its purpose is to contribute to the research and development of advanced information science and technology for Brainware systems which realize a seamless fusion of the changeable and complex real world and the cyber space.

We aim at establishing scientific and technological foundations for Real-World Computing (section), New Paradigm VLSI System (section), Intelligent Nano-Integration System (section), Microarchitecture (section), Cyber Robotics (planned section), and Next-Generation Human Interface (planned section). The Laboratory for Brainware Systems consists of the above six sections which cooperatively carry out the research. At the same time they serve as a laboratory for nation-wide cooperative research in the field of Brainware systems.

The technology developed in the Laboratory is expected to enhance the research carried out in the four Divisions of the Institute, and the research conducted in the Divisions, in turn, is expected to provide scientific basis for the information technology developed in the Laboratory.

[Research Target]

Real-World Computing Section: Our main aim is to understand highly harmonic and autonomous biological-information systems, and to propose a new system designing principles. For carrying out various purpose and functions in the real-world, the biological system must solve inverse problems. Since in general the inverse problem is an ill-posed one, the system has to create an appropriate constraint for solving the ill-posedness by itself and autonomously satisfy the created constraint in real time. Clarifying logic and basic mechanisms of “Constraint Self-Emergence and Self-Satisfaction”, we create artificial systems for pattern recognitions and motion controls that work well in the real-world.

New Paradigm VLSI System Section: Performance degradation of SoCs due to wiring complexity, power dissipation and characteristic variation of materials/devices is increasingly getting a serious problem in recent era. Our research activity is to solve the above problem by the following two ways: the use of logic-in-memory architecture based on nonvolatile logic, and the use of asynchronous data-transfer scheme based on multiple-valued current-mode logic, which would open up a novel VLSI chip paradigm, called a “new-paradigm VLSI system.”

Intelligent Nano-Integration System Section: Our research activities cover the fields of architectures of Brain computing systems, characterization and application of artificial neural networks, computer aided designs and fabrications of intelligent integrated circuits, and exploitation of new devices for
neural circuits. At present research is focused on the large scale integration of Brain computing system and exploitations of new neural devices proposing a neuromorphic quantum computation.

**Microarchitecture Section**: The research activities in microarchitecture lab. include architecture and circuit design of mixed-signal SoC applicable to sensor network system to explore brain activity research along with mixed-signal topdown design methodology.

**[Research Activities]**

**Real-World Computing Section (Yano Laboratory)**: We obtained the following three main results. First, we mathematically analyzed our adaptive, autonomous decentralized model for arm reaching movement in which each joint command is determined through mobility-based interactions among joints, and clarified that the model includes two different type controllers and they are switched in real-time depending on difference of mobility among joints. Second, using multi-electrodes, we measured neural activity of the primate prefrontal cortex during a path-planning task, and found that synchronous neural activity in the prefrontal cortex is critical for the animal to put a motion plan into practical action. Third, for clarifying a fundamental mechanism of sensory information processing in brain, we use the mini-brain of terrestrial slug Incilaria fruhstorferi, and found that there are two olfactory pathways having different dynamics of each, and that their interactions would be important for the olfactory information processing in real time.

**New Paradigm VLSI System Section (Hanyu Laboratory)**: As a this-year research result in the nonvolatile logic-in-memory technology, we have succeeded the chip fabrication of MTJ/CMOS-hybrid a compact cell circuit for a nonvolatile TCAM (ternary content-addressable-memory), and confirmed the basic behavior of its logic operation. We have also designed and fabricated a high-throughput asynchronous multiple-valued single-track data-transfer test chip, whose performance is evaluated to be 1.5-times faster than that of a conventional hardware with maintaining less wire counts.

**Intelligent Nano-Integration System Section (Nakajima-Sato Laboratory)**: By using high-order synapses for an inverse function delayed neural network, we demonstrated that optimal solutions for traveling salesman problems were obtained as stationary states like N-Queen problems. Furthermore, we have also proposed new analyses for nonlinear dynamics, in which a new concept based on the relationship between a potential and active regions was introduced. We evaluated the quality factor of a Bi-2212 intrinsic Josephson junction from its resonant activation property, and found the required condition for observing Rabi oscillation. We studied numerically the computational power of our neuromorphic quantum computation algorithm as a function of the magnitude of decoherence. The result shows that the computational power decreases in proportion to decoherence, but the degradation is smaller than other quantum algorithms. A 4-bit parallel multiplier using a carry look-ahead adder was demonstrated successfully with Nb integrated circuits to improve the performance of high-speed operation for the single flux-quantum fast Fourier transform. A 4-bit high-speed up/down counter for the neural computation using stochastic logic was fabricated and successfully demonstrated.

**Microarchitecture Section (Masui Laboratory)**: We have been investigating architecture and circuit techniques for low-power and low-cost CMOS transceiver ICs applicable to wireless sensor network. We have been explored a low-power fractional-N PLL design and a complex bandpass filter in low-IF architecture for software-defined radio applications. A design tool for fractional-N PLL optimizing an integrated loop filter considering influences of VCO and sigma-delta modulator noises to entire PLL phase noise is under development. In terms of design productivity enhancement, we have investigated the gm/Id lookup table design optimization methodology for OTA (operational transconductance amplifier), and have succeeded to generate an optimization flow for high-gain and high-speed OTA with a complex topology.
Real-world computing
Problem solving and neuronal synchrony in monkey prefrontal cortex

A schematic image of a monkey executing a maze task (left). Selectivity for final (red line), immediate (blue line) goals and neuronal synchrony (black line) (right) [1].

[Research Target and Activities]
It is unclear how biological systems behave adaptively in the real world, even though the environment is inherently indefinite and unpredictable. To clarify the adaptive mechanism underlying the brain, we study it in various fields including vision, audition, motor control and memory.

In this year, to investigate the temporal relationship between synchrony in the discharge of neuron pairs and modulation of the discharge rate, we recorded the neuronal activity of the lateral prefrontal cortex of monkeys performing a behavioral task that required them to plan an immediate goal of action to attain a final goal. Information about the final goal was retrieved via visual instruction signals, whereas information about the immediate goal was generated internally (left figure). The synchrony of neuron pair discharges was analyzed separately from changes in the firing rate of individual neurons during a preparatory period. We focused on neuron pairs that exhibited a representation of the final goal followed by a representation of the immediate goal at a later stage. We found that changes in synchrony and discharge rates appeared to be complementary at different phases of the behavioral task. Synchrony was maximized during a specific phase in the preparatory period corresponding to a transitional stage when the neuronal activity representing the final goal was replaced with that representing the immediate goal (right figure). We hypothesize that the transient increase in discharge synchrony is an indication of a process that facilitates dynamic changes in the prefrontal neural circuits in order to undergo profound state changes.

[Staff]
Professor             YANO Masafumi Dr.
Assistant professors  MAKINO Yoshinari Dr., SAKANOTO Kazuhiro Dr.
Research associates  TOMITA Nozomi Dr.

[Profile of Professor YANO]
1992-: Professor, Research Institute of Electrical Communication, Tohoku University.

[Papers]
Intelligent Nano-Integration System
Basic Technology of Integrated System for Intelligent Processing

[Research Target and Activities]

Our research activities cover the fields of architectures of Brain computing systems, characterization and application of artificial neural networks, computer aided designs and fabrications of intelligent integrated circuits, and exploitation of new devices for neural circuits.

We have constructed a stochastic artificial neural network with one million synaptic units, analyzed the dynamic behaviour of neural networks aiming at a time-dependent data processing, succeeded to propose a system where we are able to get off successfully from any local minima fallen into on the way of data processing in neural networks, and fabricated its prototype hardware system on the silicon microchip by using the CMOS technology. We have also presented an FFT and a neural system operated by using a flux quantum logic in superconducting integrated circuits.

At present research is focused on the large scale integration of Brain computing system and exploitations of new neural devices proposing a neuromorphic quantum computation.

Research Activities in 2008

1) By using high-order synapses for an inverse function delayed neural network, we demonstrated that optimal solutions for traveling salesman problems were obtained as stationary states like N-Queen problems. Furthermore, we have also proposed new analyses for nonlinear dynamics, in which a new concept based on the relationship between a potential and active regions was introduced. 2) We evaluated the quality factor of a Bi2212 intrinsic Josephson junction from its resonant activation property, and found the required condition for observing Rabi oscillation. We studied numerically the computational power of our neuromorphic quantum computation algorithm as a function of the magnitude of decoherence. The result shows that the computational power decreases in proportion to decoherence, but the degradation is smaller than other quantum algorithms. 3) A 4-bit parallel multiplier using a carry look-ahead adder was demonstrated successfully with Nb integrated circuits to improve the performance of high-speed operation for the single flux-quantum fast Fourier transform. A 4-bit high-speed up/down counter for the neural computation using stochastic logic was fabricated and successfully demonstrated.

[Staff]
Professor NAKAJIMA Koji
Dr. Associate Professor SATO Shigeo
Dr. Assistant Professor ONOMI Takeshi

[Profile of Professor NAKAJIMA]
K. Nakajima was received his B.E. M.E. and Dr. Eng. from Tohoku University, Sendai, Japan, in 1972, 1975, and 1978, respectively. Since 1978, he has been working at the Research Institute of Electrical Communication, Tohoku University, except for a ten month period in 1983 when he was a Visiting Assistant Research Engineer at the University of California, Berkeley. He is a professor at the Research Institute of Electrical Communication, Tohoku University, and is currently engaged in the study of VLSI implementation of neural network, and Josephson Junction devices for digital applications.

[Papers]

Contact to Professor Koji NAKAJIMA : hello@nakajima.riec.tohoku.ac.jp
Ubiquitous society has been established by the deployment of various wireless systems ICs, and it demands advances in mixed-signal (analog and digital) design technique as well as higher integration through SoC (System on a Chip). Our research activities include architecture and circuit design of mixed-signal SoC applicable to sensor network systems for the investigation of brain activities researches as well as design automation of RF/analog circuit. We have developed a complex bandpass filter for low-IF architecture in a transceiver IC for sensor network systems and been expanding this technology to software defined radio. Moreover, we have been researching on the OTA design optimization flow by using gm/Id based lookup table methodology.

[Staff]
Professor: MASUI Shoichi Dr.

[Profile of Professor MASUI]
Shoichi Masui received the B. S. and M. S. degrees from Nagoya University, Nagoya, Japan in 1982, and 1984, respectively, and received the Ph. D. degree from Tokyo Institute of Technology in 2006. From 1990 to 1992, he was a Visiting Scholar at Stanford University, Stanford CA, and in 2001, he was a Visiting Scholar at University of Toronto, Toronto ON, Canada. Since 2007, he is a professor in Research Institute of Electrical Communication, Tohoku University. He was the recipient of a commendation by the Minister of Education, Culture, Sports, Science, and Technology, Japan, in 2004 for his research achievements on FeRAM.

[Papers]
New Paradigm VLSI System Research Group

Realization of a New-paradigm VLSI System

[Research Target and Activities]
Rapid progress in recent deep submicron regime has led the capability to realize giga-scaled embedded systems on a chip (SoC), while performance degradation of SoCs due to wiring complexity, power dissipation and device-characteristic variation is increasingly getting a serious problem in the recent VLSI chip. Our research activity is to solve the above problem primarily by the following two ways: the use of logic-in-memory architecture based on nonvolatile logic, and the use of asynchronous data-transfer scheme based on multiple-valued current-mode logic, which would open up a novel VLSI chip paradigm, called a “new-paradigm VLSI system.”

As a this-year research result in nonvolatile-logic area, we have succeeded the chip fabrication of MTJ(Magnetic Tunnel Junction)-based basic logic component, a compact cell circuit for nonvolatile TCAM (ternary content-addressable-memory) (Fig.3). We have also designed and fabricated a highly reliable multiple-valued current-mode multiplier chip (Fig. 1) and a high-throughput asynchronous multiple-valued single-track data-transfer test chip (Fig. 2), whose performance is evaluated to be 1.5-times faster than that of a conventional implementation with maintaining less wire counts..

[Staffs]
Professor HANYU Takahiro Dr.
Assistant Professor MATSUMOTO Atsushi Dr.
Assistant Professor NATSUI Masanori Dr.

[Profile of Professor HANYU]
Takahiro Hanyu received the B.E., M.E. and D.E. degrees in Electronic engineering from Tohoku University, Sendai, Japan, in 1984, 1986, 1989, respectively. He is currently a Professor in the Research Institute of Electrical Communication, Tohoku University. His general research interests include multiple-valued current-mode logic and its application to high performance and low-power arithmetic VLSIs.

[Papers]

Fig. 1: Highly-reliable 16-bit MVCM multiplier chip.
Fig. 2: Multiple-valued current-mode single-track-data-transfer chip
Fig. 3: MTJ-based nonvolatile TCAM

Contact to Professor Takahiro HANYU : hanyu@riec.tohoku.ac.jp
IT-21 center

Research and Development of the IT-Based Practical Technology
by the Industry-Academia-Government Collaboration

[Research Target and Activities]

The purpose of the IT-21 center is development of practical technologies for IT based on the advanced technologies of RIEC with the partnership among Industry, Government and University. The term of development is limited less than 5 years. The projects are planned on matching with both basic technologies in the University and application in the Industry. Combination of the technologies of the University and Industry makes practical technologies with availability for the commercial products. The center actively accelerates to obtain the intellectual properties generated from the development of practical technology to the Industry. From 2007, the new 2 projects were started.

1. Development of Dependable Wireless System and Devices

Our new project “Development of Dependable Wireless System and Devices” was accepted in 2007 as the Japan Science and Technology Agency (JST) CREST type research program “Fundamental Technology for Dependable VLSI System.” The project has been executed by the collaborations between RIEC including IT21 mobile wireless technology group, major Japanese mobile terminal manufacturers and other universities. It aims for development of (1) all IP dependable wireless network which can realize a communication speed of 1Mbit/s~10Gbit/s, (2) all Si CMOS mixed signal LSI with frequency range of 500MHz~70GHz, (3) frequency domain equalizer technology, and (4) scalable AD converter.

2. Development of Low Power Consumption Mass Storage HDD Systems

A new project “Development of super high-speed mass storage HDD systems” started in 2007 under the collaborations between RIEC including IT21 storage technology group, major Japanese HDD manufacturers and other laboratories within Tohoku University. The goals of this project are to develop the perpendicular recording technologies required for higher than 2 Tbits/inch² recording density and, based on these technologies, to develop the system architecture for realizing large capacity, high performance and low power consumption storage systems: (1) Development of fundamental technologies for the recording densities over 2 Tb/inch²: high sensitivity sensors, high recording resolution SPT writers and high-density media including patterned media, (2) Development of a system architecture for high performance and low power consumption storage systems.

[Staff]

Director: Professor TSUBOUCHI Kazuo, Dr.
Technical Official: SAGAE Katsumi

Project Planning Division
Professor: FURUNISHI Makoto, Dr.

Technology Development Division (Mobile Wireless Technology Group)
Professor: TAKAGI Tadashi Dr.
Visiting Professor: IWATA Makoto Dr.

Technology Development Division (Storage Technology Group)
Professor: FUJIMOTO Kazuhisa Dr.
Professor: AOI Hajime Visiting Dr.
Associate Professor: SHIMATSU Takehito Dr.
Visiting Associate Professor: YAMAKAWA Kiyoshi Dr.
The IT-21 Center Project Planning Division

Planning and Encouraging of R&D Projects

[Research Target and Activities]
Study on trends of science and technology policy, etc. to launch R&D projects with industries. Study on schemes to support R&D projects.

We have successfully launched two new projects since 2007FY, “the development of super high-speed mass storage HDD systems” and “the development of CMOS wireless LAN by 3D SiP”. We issued a final report on the IT Project which was entrusted by the Ministry of Education, Culture, Sports, Science and Technology (MEXT) from 2002FY to 2007FY. Planning of a new project is underway to utilize the internationally standardized outcome of the IT Project.

We have proposed the roles that the Research Institute of Electrical Communication (RIEC) would play in the management of intellectual property rights, and have arranged a new scheme to handle intellectual property rights with industries in order to carry out the projects mentioned above.

We hold meetings with institutes with which RIEC has concluded the cooperation agreements to start R&D projects.

[Staff]
Professor : FURUNISHI, Makoto
Secretary : KAGAMITANI, Machiko

[Profile of Professor FURUNISHI]
1986.3 got master’s degree from Department of Mechanical Engineering, Faculty of Engineering, the University of Tokyo
1986.4 joined Science and Technology Agency (Ministry of Education, Culture, Sports, Science and Technology (MEXT) at present)
2005.1- 2004.7 Director of Nuclear Fuel Cycle Regulation Division, NISA, METI
2006.8- Professor, IT-21 Center, RIEC, Tohoku University, 2006.11-2008.3 Special Advisor to President

Contact to Professor Makoto FURUNISHI : makoto.furunishi@it21.riec.tohoku.ac.jp
IT21 Center Mobile Wireless Technology Group

Development of DWS (Dependable Wireless System) Technology for DWN (Dependable Wireless NGN)

[Research Target and Activities]
1. All Si CMOS RFIC
(1) We have developed a 5 GHz band Si CMOS VCO (Voltage Control Oscillator) having a novel resonant circuit which can suppress 1/f noise from bias circuit and capacitance fluctuation of varactor by harmonics. The VCO has performed tuning range of 17.9% and phase noise of -110.8dBc @ 1MHz offset over full tuning range, which is the world top class performance.
(2) We have developed a novel design technology of FET gate width optimization and high accurate device modeling in 60GHz frequency range. Using this technology, we have developed a 60GHz band Si CMOS 2-stage HPA (High Power Amplifier), which has successfully performed gain of 8dB over 59~66GHz, output power of 8.8dBm with efficiency of 14.5%.
2. Digitally Assisted Compensation Technology
We have developed a novel FDE (Frequency Division Equalizer) technology implemented to a FPGA (Field Programmable Gate Array). We have demonstrated a transmission test under multi-pass fading environments. Due to the FDE, we have realized to improve BER (Bit Error Rate) characteristics. Conventionally, FDE technology has been evaluated by simulation. Here, we have been able to realize it by experiment.
3. Adaptive and Scalable ADC/DAC (Analog to Digital Converter/ Digital to Analog Converter)
We have devised a current mode pipeline ADC, which is suitable for process miniaturization and low supply voltage. We have designed several core circuits of the ADC and have realized static characteristics.

[Staff]
Professor: TAKAGI, Tadashi, Dr. (since 2005)
Guest Professor: IWATA, Makoto, Dr. (since 2003)

[Profile of Professor TAKAGI, Tadashi]
Professor TAKAGI Tadashi received the B.S. degree in physics from Tokyo Institute of Technology, Tokyo, Japan and Ph.D. degree in electronic engineering from Shizuoka University, Shizuoka, Japan, in 1973 and 1995, respectively. In 1973, he joined the Mitsubishi Electric Corporation, where he was engaged in development on microwave and millimeter-wave circuits technology. Since 2005, he has been with Tohoku University, where he is now a professor. Now, his main area of research interest is mobile wireless broadband communication circuits and systems technology. He is a senior member of the IEEE and a member of IEICE of Japan

[Papers]
IT-21 center, Technology Development Division, Storage Technology Group

Development of low power consumption mass storage HDD systems

[Research Target and Activities]
A new project :Development of super high-speed mass storage HDD systems started in August 2007 under the collaborations between RIEC including IT21 storage technology group, major Japanese HDD manufacturers. The goals of this project are to develop the perpendicular recording technologies required for higher than 2 Tbits/inch² recording density and, to develop the system architecture for realizing large capacity, high performance and low power consumption storage systems.

This year we continued fundamental studies and obtained the following achievements.

1) High density recording media: L11-type CoNiPt ordered alloy films with a large uniaxial magnetic anisotropy of the order of $10^7$ erg/cm³ and relatively low saturation magnetization were successfully fabricated using UHV sputter film deposition. Moreover, we experimentally showed that the surface anisotropy enhances the thermal stability of hard/soft stacked dot arrays.

2) High sensitivity sensor technology: New process for high sensitivity spin accumulation sensor structure was developed. And MgO barrier as a high spin polarization material was investigated.

3) Single pole type writer with a high recording resolution: Optimization of multiple tapered main-pole structure is carried out utilizing simulation. Process for shaping main-pole was started.

4) New recording algorithm for over terabits per square inch densities: Possibility of attaining 2Tb/in² using hard/soft stacked structure bit patterned media (BPM) was revealed using simulation. Investigations of energy assist recording method utilizing BPM and two dimensional magnetic recording method utilizing granular media were started for attaining over 2Tb/in².

5) High performance tiered storage system: From the power consumption estimation based on the measurement results using prototype, it was showed that “2-dimension data allocation method with an access prediction” is possible to reduce the storage system power consumption by half. We started developments of control software.

[Staff]
Professor: FUJIMOTO Kazuhisa Dr. Visiting Professor: AOI Hajime Dr.
Associate Professor: SHIMATSU Takehito Dr. Visiting Associate Professor: YAMAKAWA Kiyoshi Dr.

[Profile of Professor FUJIMOTO]
Kazuhisa Fujimoto received the Dr. of Engineering degree from Kyushu University in 1997. He joined Central Research Laboratory, Hitachi, Ltd., in 1987. He joined RIEC, Tohoku University in 2007. He has been engaged in research on storage system architectures.

[Papers]

Contact to Professor Kazuhisa FUJIMOTO : fujimoto@riec.tohoku.ac.jp
Management Office for Health and Safety
Realizing and Maintaining a Safe and Comfortable Environment to Support Research

[Research Target and Activities]

Safety and health seminar       First aid training course

1. Outline of the Management Office for Health and Safety
The Management Office for Health and Safety is established to maintain the health and safety of students and staff working at the institute. The use of chemicals, high-pressure gas and radiation in research activities at the institute entails many risks. The Management Office for Health and Safety provides support for health and safety management in research laboratories, experimental facilities and the Fundamental Technology Center through various activities to ensure safe and smooth research activities within the institute.

2. Activities by the Management Office for Health and Safety
For the actual management of health and safety at the office, the Health and Safety Committee first presents the basic policies of safety management at the institute, and the Management Office for Health and Safety then plans and executes activities based on them. At the institute, laboratories and other individual sections are highly independent of each other; unlike a general corporate organization, top-down safety management is not suitable and measures appropriate for independent sections need to be taken. Various considerations are also necessary for students, researchers, and other members engaged in research activities as well as faculty staff. At this institute, extremely hazardous materials and facilities are used, including chemicals, high-pressure gas, and X-ray devices. Since there is also a clean room and other special workplaces, safety management should be extended by considering them. In these circumstances, the Management Office for Health and Safety will monitor situations and characteristics in each section at the institute, plan and recommend practical management methods and improvement measures, and support their implementation for the efficient and effective management of health and safety. The main activities in this fiscal year are as follows:

- Holding safety and health seminar and high-pressure gas seminar for staff and students at the institute
- Inspection of and assistance in improving the health and safety management system and working environment within the institute
- Holding first-aid training course
- Investigation of laws related to health and safety and collection of information regarding health and safety management
- Providing advice and information to health and safety personnel in each department

[Staff]
Manager: NIWANO Michio Dr., Professor
Deputy Manager: UEHARA Yoichi Dr., Professor
SATO Nobuyuki Dr., Assistant Professor
CHIBA Ayako, Clerk

Contact to: anzen@riec.tohoku.ac.jp
Flexible Information System Research Center
Development of Flexible Information Systems and Management of Network

[Research Target and Activities]
The present information systems such as computers are inflexible systems, because their purpose is predefined and they provide only the fixed procedures and functions. On the other hand the flexible information system can perform the flexible information processing adapted to the human intention and situation of its environment.

Our goal is to investigate principles of the flexible information processing through the theories and experiments, and establish their system construction methodology. Moreover, we also study the flexible distributed systems for advanced organization, utilization, administration, operation and putting out scientific information. Through practical applications of above results to the real network in RIEC, we confirm effectiveness of our methods. To achieve the above goal, this year we have conducted the following researches: (1) development of distributed and scalable authentication method for large scale overlay network, (2) development of an agent based network management system (Fig.1) and (3) improvement of accuracy of virtual auditory display system, and flexible computing mechanism in biological systems.

[Staff]
(1) Steering Committee
Professor: SUZUKI Yôiti Dr., SHIRATORI Norio Dr., TOYAMA Yoshihito Dr., KINOSHITA Tetsuo Dr., SHIRAI Masafumi Dr., OHORI Atsushi Dr.
(2) FIR Committee
Professor: TOYAMA Yoshihito Dr., KINOSHITA Tetsuo Dr.
Associate Professor: AOTO Takahito Dr., KITAGATA Gen Dr.
Assistant Professor: HAYAKAWA Yoshihiro Dr., YOSHIDA Masato Dr., SASANO Isao Dr., YAIRI Satoshi Dr.
Research Fellow: SASAI Kazuto Dr.
Technical Support Member: DAIGAKU Noriko / KOBAYASHI Kenichi, SUZUKI Midori, NIITSUMA Sachiko
(3) Regular Staff
Associate Professor: KITAGATA Gen Dr.
Assistant Professor: YAIRI Satoshi Dr.
Research Fellow: SASAI Kazuto Dr.
Technical Support Member: DAIGAKU Noriko / KOBAYASHI Kenichi, SUZUKI Midori, NIITSUMA Sachiko

[Profile of Professor SUZUKI]
Refer to the Advanced Acoustic Information Systems Laboratory for the profile Prof. Yôiti Suzuki.

[Papers]
Fundamental Technology Center
Supporting research with high-level specialized knowledge and technology

Overview of Fundamental Technology Center

[Research Target and Activities]
The Fundamental Technology Center provides research support, closely linked with research activities at the Institute, based on its high-level specialized knowledge and technology. We must maintain and improve our organization to develop and pass on high-level knowledge and technology. Based on this philosophy, the Center provides research support through its four Divisions.

1. Machine Shop Division
The Machine Shop Division has previously pioneered a number of new machining methods, and contributed to research on high-density magnetic recording and many other types of research relating to advanced information and communications. This year, there were 123 fabrication requests from laboratories (128 from inside the Institute, 10 from outside).

2. Evaluation Division
The Evaluation Division provides the following as measurement equipment for shared use: Apparatus for Rutherford backscattering spectroscopy, atomic force microscope, scanning electron microscope, X-ray diffractometer, electron spin resonance spectrometer, fourier transform infrared spectrophotometer, infrared-visible spectroscope, liquid chromatograph, optical characteristic measurement equipment, in-air photoelectron measurement equipment, dicing saw.

3. Process Division
The Process Division provides the following as measurement equipment for shared use: Focused ion beam system, mask aligner, X-ray diffractometer for thin films, electron beam exposure equipment, scanning electron microscope, optical microscope, scanning probe microscope, spectrophotometer, digital microscope, dicing saw, UV & ozone dry stripper.

4. Software Technology Division
The Software Technology Division manages, operates and develops information systems for the Institute. To support research in each field, the Division handles tasks like disseminating information on research results and providing services for gathering/organizing/utilizing academic information, such as space collaboration systems and a database of RIEC researchers.

[Staff]
Director (Professor): NIWANO Michio Dr., Assistant Professor: SATO Nobuyuki Dr.
Technical Official : TAKYU Choichi, AGATSUMA Shigeto, SHOJI Koichi, SUENAGA Tamotsu, YAMASHITA Takeshi, ABE Maho, WATANABE Hiroshi, SUGAWARA Munetomo, KONNO Yuji, YONEZAWA Ryuji

Contact to : eac@riec.tohoku.ac.jp
4. Nation-wide Cooperative Research Projects

The Institute has a long history of fundamental contributions in many fields of engineering and science that include the fields of semiconductor materials and devices, magnetic recording, optical communication, electromagnetic technology, applications of ultrasonic, acoustic communication, non-linear physics and engineering, and computer software. On the basis of this rich historical background the Institute was designated as National Center for Cooperative Research in 1994. Accompanying Tohoku University’s transformation to “a national university juridical entity” in April, 2004, this institution plays a leading role on the world stage, as its researchers, both domestic and foreign, continue the task of “investigating the theory and application of universal science and technology to realize communication, to the enrichment of humanity.”

In such background, the Institute organizes Nation-wide Cooperative Research Projects by coordinating its activities with research workers. The main themes for Cooperative Research are selected annually by the Committee for Cooperative Research. Then invitations for project proposals and participation are extended to university faculties and government laboratories as well as industrial research groups. Each project approved by the Faculty Council of the Institute is carried out by a team of researchers that include members of the Institute as well as outside participants.

The advisory Council which includes members from other institutions has an advisory function to the Director in defining the general direction of the research at the Institute and its Nation-wide Cooperative Research Projects.

The Project Judging Committee that includes members from the outside of Tohoku University has a judging function for project proposals. The purpose of the Project Steering Committee is the proper operation of approved projects.
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<td>Victor, RYZHII The University of Aizu Computer Science Division</td>
<td>Taiichi OTSUJI</td>
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<td>H18/A02</td>
<td>Study on the improvement of performance and reliability in nonclassical group-IV semiconductor hetero-devices</td>
<td>Toshiaki TSUCHIYA Shimane University Interdisciplinary Faculty of Science and Engineering</td>
<td>Junichi MUROTA</td>
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<td>H18/A03</td>
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<td>H18/A04</td>
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<td>H18/A12</td>
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<td>Jun-ichi KUSHIBIKI School of Engineering Tohoku University</td>
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<td>H18/A13</td>
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<td>Satoshi SHIOIRI Research Institute of Electrical Communication, Tohoku University</td>
<td>Satoshi SHIOIRI</td>
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<td>H19/A01</td>
<td>Development of magnetic-dielectric material using magnetic nanoparticle assembly for ultra-high frequency devices</td>
<td>Migaku TAKAHASHI New Industry Creation Hatchery Center, Tohoku University</td>
<td>Yasuo CHO</td>
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<td>Hiroshi UEDA Library and Information Technology Center, Gunma University</td>
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<td>Study on the representation of color signal in visual cortex</td>
<td>Ichiro KURIKI Research Institute of Electrical Communication, Tohoku University</td>
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<td>Development of new measurement techniques with nanometer scale spatial resolution and exploration of the electronic and optical properties of surface nanostructures</td>
<td>Yoichiro UEHARA Research Institute of Electrical Communication, Tohoku University</td>
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<td>Dynamics of semiconductor gate-stack nanostructure formation and its applications</td>
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<td>A proof theoretical approach to principles of program construction</td>
<td>Masahiko SATO</td>
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5. Symposium organized by the Institute

This Symposium is planned to exchange relevant information on current important topics concerning Electrical Eng., Electrical Communications, Electronic Eng., and Information Eng. Many related researchers inside and outside Tohoku University participate the Symposium and stimulate discussion.

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<td>2 Ultra-High Frequency Acoustoelectronics</td>
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<td>3 Artificial Intelligence</td>
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<td>4 Thin Film Electronics</td>
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<td>7 Current Status and Future Trends of Superconductivity</td>
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<tr>
<td><strong>5</strong> International Workshop on Photonic and Electromagnetic Crystal Structures</td>
<td>Mar.8-10, 2000</td>
</tr>
<tr>
<td><strong>6</strong> Physics and Application Spin Related Phenomena in Semiconductors</td>
<td>Sep.13-15, 2000</td>
</tr>
<tr>
<td><strong>8</strong> Nonlinear Theory and its Applications</td>
<td>Oct.28-Nov.1, 2001</td>
</tr>
<tr>
<td><strong>9</strong> New Paradigm VLSI Computing</td>
<td>Dec.12-14, 2002</td>
</tr>
<tr>
<td><strong>11</strong> 3rd International Workshop on New Group IV (Si·Ge·C) Semiconductors</td>
<td>Oct.12-13, 2004</td>
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<tr>
<td><strong>12</strong> 3rd International Workshop on High Frequency Micromagnetic Devices and Materials (MMDM3)</td>
<td>Apr.11-12, 2005</td>
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<tr>
<td><strong>13</strong> 4th International Conference on Silicon Epitaxy and Heterostructures (lCSl-4)</td>
<td>May.23-26, 2005</td>
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<tr>
<td>No.</td>
<td>Event Description</td>
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<tr>
<td>-----</td>
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<tr>
<td>14</td>
<td>1st International WorkShop on New Group IV Semiconductor Nanoelectronics</td>
</tr>
<tr>
<td>15</td>
<td>GSIS International Symposium on Information Sciences of New Era: Brain, Mind and Society</td>
</tr>
<tr>
<td>16</td>
<td>The 1st RIEC International Workshop on Spintronics ‘Spin Transfer Phenomena’</td>
</tr>
<tr>
<td>17</td>
<td>4th International Workshop on High Frequency Micromagnetic Devices and Materials (MMDM4)</td>
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<tr>
<td>20</td>
<td>2nd RIEC International Workshop on Spintronics</td>
</tr>
<tr>
<td>21</td>
<td>Japan-China Joint Conference on Acoustics, JCA2007</td>
</tr>
<tr>
<td>23</td>
<td>The 3rd RIEC International Workshop on Spintronics</td>
</tr>
<tr>
<td>27</td>
<td>International Interdisciplinary-Symposium on Gaseous and Liquid Plasmas (ISGLP 2008)</td>
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<tr>
<td>29</td>
<td>The 4th RIEC International Workshop on Spintronics</td>
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</tbody>
</table>
6. Study groups on Electrical Communication

Study Groups on Electrical Communication are organized to solve scientific and technological problems and to promote research and development through the collaboration of the Research Institute of Electrical Communication, Depts. of Electrical Eng., Electrical Communications, Electronic Eng., Information Eng., and related scientists and engineers inside and outside Tohoku University. The Study Groups on Electrical Communication consist of 14 Sub-Groups as listed below, to deal with specific subjects. Each Sub-Group holds workshops. The abstracts of the workshops are published annually in *The Record of Electrical and Communication Engineering Conversazione Tohoku University*.

Many scientists and engineers—not only from universities but also from government laboratories and industries—attend the workshops, present papers, and discuss issues actively. We are pleased to provide information on these activities upon request. Please contact each Sub-Group Chairman or manager for general information or more specific questions.

<table>
<thead>
<tr>
<th>Electromagnetic and Optical Waves Engineering</th>
</tr>
</thead>
<tbody>
<tr>
<td>Chair</td>
</tr>
<tr>
<td>Manager</td>
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<table>
<thead>
<tr>
<th>Acoustic Engineering</th>
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<tr>
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<td>Manager</td>
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<table>
<thead>
<tr>
<th>Sendai &quot;Plasma Forum&quot;</th>
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</thead>
<tbody>
<tr>
<td>Chair</td>
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<td>Manager</td>
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<table>
<thead>
<tr>
<th>Sendai Seminar on EMC</th>
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<tr>
<td>Chair</td>
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<tr>
<td>Manager</td>
</tr>
</tbody>
</table>
### Computer Science

<table>
<thead>
<tr>
<th>Position</th>
<th>Name</th>
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</thead>
<tbody>
<tr>
<td>Chair</td>
<td>Prof. Naoki KOBAYASHI</td>
</tr>
<tr>
<td>Manager</td>
<td>Associate Prof. Takahito AOTO</td>
</tr>
</tbody>
</table>

### Systems Control

<table>
<thead>
<tr>
<th>Position</th>
<th>Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>Chair</td>
<td>Prof. Makoto YOSHIZAWA</td>
</tr>
<tr>
<td>Manager</td>
<td>Associate Prof. Takashi WATANABE</td>
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### Information-biontronics

<table>
<thead>
<tr>
<th>Position</th>
<th>Name</th>
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<tbody>
<tr>
<td>Chair</td>
<td>Prof. Michio NIWANO</td>
</tr>
<tr>
<td>Manager</td>
<td>Prof. Tatsuo YOSHINOBU</td>
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### Spinics

<table>
<thead>
<tr>
<th>Position</th>
<th>Name</th>
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<tbody>
<tr>
<td>Chair</td>
<td>Prof. Kazushi ISHIYAMA</td>
</tr>
<tr>
<td>Manager</td>
<td>Associate Prof. Fumihiro SATO</td>
</tr>
<tr>
<td>Manager</td>
<td>Assistant Prof. Shuichiro HASHI</td>
</tr>
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### New Paradigm Computing

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<tr>
<th>Position</th>
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<tr>
<td>Chair</td>
<td>Prof. Michitaka KAMEYAMA</td>
</tr>
<tr>
<td>Manager</td>
<td>Assistant Prof. Naofumi HOMMA</td>
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### Ultrasonic Electronics

<table>
<thead>
<tr>
<th>Position</th>
<th>Name</th>
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<tbody>
<tr>
<td>Chair</td>
<td>Prof. Shinichirou UMEMURA</td>
</tr>
<tr>
<td>Manager</td>
<td>Assistant Prof. Shin YOSHIZAWA</td>
</tr>
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</table>
### Integration of Brain Functions

<table>
<thead>
<tr>
<th>Role</th>
<th>Name</th>
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</thead>
<tbody>
<tr>
<td>Chair</td>
<td>Prof. Koji NAKAJIMA</td>
</tr>
<tr>
<td>Manager</td>
<td>Associate Prof. Shigeo SATO</td>
</tr>
</tbody>
</table>

### Mathematical Physics and its Application to Information Sciences

<table>
<thead>
<tr>
<th>Role</th>
<th>Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>Chair</td>
<td>Prof. Kazuyuki TANAKA</td>
</tr>
<tr>
<td>Manager</td>
<td>Prof. Kazuyuki TANAKA</td>
</tr>
</tbody>
</table>

### Biocybernetics and Bioinformatics

<table>
<thead>
<tr>
<th>Role</th>
<th>Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>Chair</td>
<td>Prof. Mitsuyuki NAKAO</td>
</tr>
<tr>
<td>Manager</td>
<td>Assistant Prof. Yoshinari MAKINO</td>
</tr>
</tbody>
</table>

### Nanoelectronics and Spintronics

<table>
<thead>
<tr>
<th>Role</th>
<th>Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>Chair</td>
<td>Prof. Hideo OHNO</td>
</tr>
<tr>
<td>Manager</td>
<td>Associate Prof. Yuzo OHNO</td>
</tr>
</tbody>
</table>
7. International activities

Many of the staff in RIEC contribute to the development of technology and science in the world by serving as editors of referees of international journals or by chairing or programming international conferences. In some fields in electronics, electrical communications, or information engineering RIEC serves as a Center of Excellence (COE), which attracts many visiting researchers and students from all over the world every year. Several academic exchange programs with foreign colleges or institutes are in operation.

**International academic exchange programs:**

- The Institute of Physics, Polish Academy of Sciences (Poland)
- The Faculty of Science, Chulalongkorn University (Thailand)
- Harbin Institute of Technology (China)
- The James Frank Institute, The University of Chicago (U.S.A.)
- Queen Mary and Westfield College, University of London (U.K.)
- Scientific Research Department, Shenzhen University (China)
- Institute of Information and Communication Technology, Sung-Kyun-Kwan University (Korea)
- Institute of Materials Science, Faculty of Applied Physics, University of Twente (Netherlands)
- The Institute of Radioengineering and Electronics Russian Academy of Sciences (Russia)
- Department of Electronics Science and Engineering, University of Nanjing (China)
- School of Computer and Communication Engineering, Taegu University (Korea)
- Research Center of Condensed Materials and Nanosciences, National Center for Scientific Research (France)
- IHP-Innovations for High Performance microelectronics (Germany)
- Institute of Semiconductors Chinese Academy of Sciences (China)

**International journals in which a staff in RIEC participates as an editor:**

<p>| | |</p>
<table>
<thead>
<tr>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Acoustical Science and Technology</td>
</tr>
<tr>
<td>2</td>
<td>Applied Acoustics</td>
</tr>
<tr>
<td>3</td>
<td>Electronics Express</td>
</tr>
<tr>
<td>5</td>
<td>International Journal of Infrared, Millimeters, and terahertz Waves</td>
</tr>
<tr>
<td>7</td>
<td>Journal of Applied Physics</td>
</tr>
<tr>
<td>8</td>
<td>Journal of Communications and Network (JCN)</td>
</tr>
<tr>
<td>9</td>
<td>Journal of Higher - Order and Symbolic Computation</td>
</tr>
<tr>
<td>10</td>
<td>Journal of Magnetism and Magnetic Materials</td>
</tr>
<tr>
<td>11</td>
<td>Optical Fiber Technology</td>
</tr>
<tr>
<td>12.</td>
<td>Optics Communications</td>
</tr>
<tr>
<td>13.</td>
<td>Semiconductor Science and Technology, Institute of Physics</td>
</tr>
<tr>
<td>14.</td>
<td>Solid State Communications</td>
</tr>
<tr>
<td>15.</td>
<td>Superlattices and Microstructures</td>
</tr>
<tr>
<td>16.</td>
<td>Virtual Journal of Nanoscale Science and Technology, American Institute of Physics / America Physical Society</td>
</tr>
</tbody>
</table>

**Recent international conferences programmed by a staff in RIEC:**

<p>| 1. | 14th International Conference on Modulated Semiconductor Structures (MSS-14 2009) |
| 2. | 1st International Workshop on Si based nano-electronics and photonics (SiNEP-09) |
| 3. | 2009 International Conference on Solid State Devices and Materials (SSDM) |
| 4. | Asia-Pacific Conference on Vision2010 |
| 5. | 20th International Colloquium on Magnetic Films and Surfaces, 2009 |
| 6. | 2nd Semiconductor Technology for Ultra Large Scale Integrated Circuits and Thin Film Transistors (ULSIC vs. TFT) |
| 7. | 38th International School &amp; Conference on the Physics of Semiconductors &quot;Jaszowiec&quot; 2009 |
| 8. | 5th Int. Workshop on New Group IV Semiconductor Nanoelectronics |
| 9. | 5th International Conference on Molecular Electronics and Bioelectronics (M&amp;BE5) |
| 10. | 5th International School and Conference on Spintronics and Quantum Information Technology, 2009 |
| 11. | 5th RIEC Int. Workshop on Spintronics, RIEC-CNSI Workshop on Nanoelectronics, Spintronics and Photonics, 2009 |
| 12. | 6th International Conference on Silicon Epitaxy and Heterostructures (ICSI-6) |
| 14. | European Conference on Optical Communication (ECOC) |
| 15. | European Solid-State Device Research Conference (ESSDERC) |
| 16. | Fuji International Symposium on Functional and Logic Programming (FLOPS2008) |
| 17. | Global Symposium on Millimeter Waves 2009 (GSMM2009) |
| 18. | IEEE Computer Society Technical Committee on Multiple-Valued Logic (TCMVL) |
| 19. | IEEE International Magnetics Conference (INTERMAG2009) |</p>
<table>
<thead>
<tr>
<th></th>
<th>Event</th>
</tr>
</thead>
<tbody>
<tr>
<td>20</td>
<td>International Conference of Magnetism (ICM 09)</td>
</tr>
<tr>
<td>21</td>
<td>International Conference on Indium Phosphide and Related Materials (IPRM2010)</td>
</tr>
<tr>
<td>22</td>
<td>International Conference on infrared, Millimeter, and Terahertz waves</td>
</tr>
<tr>
<td>23</td>
<td>International Symposium on Graphene Devices</td>
</tr>
<tr>
<td>24</td>
<td>International Symposium on Surface Science and Nanotechnology (ISSS-5)</td>
</tr>
<tr>
<td>25</td>
<td>International Workshop on the Principles and Application of Spatial Hearing</td>
</tr>
<tr>
<td>26</td>
<td>Sixth International Conference on Physics and Application of Spin-related Phenomena in Semiconductors (PASPS) 2010</td>
</tr>
<tr>
<td>27</td>
<td>SPIE Defense and Security Conference 2009</td>
</tr>
<tr>
<td>29</td>
<td>The 11th Joint MMM-Intermag Conference 2010</td>
</tr>
<tr>
<td>30</td>
<td>The 2010 International Conference on Computational Science and Its Applications (ICCSA2010)</td>
</tr>
<tr>
<td>31</td>
<td>The 20th Personal, Indoor and Mobile Radio Communications Symposium 2009 (PIMRC '09)</td>
</tr>
<tr>
<td>32</td>
<td>The 8th Pacific Rim Conference on lasers and Electro-optics (CLEO/PR20·09)</td>
</tr>
<tr>
<td>33</td>
<td>The ACM SIGPLAN Workshop on Types in Language Design and Implementation (TLDI2009)</td>
</tr>
<tr>
<td>34</td>
<td>The Eighth Perpendicular Magnetic Recording Conference (PMRC)</td>
</tr>
<tr>
<td>35</td>
<td>The Korean Magnetic Society</td>
</tr>
<tr>
<td>36</td>
<td>The Sixth International Conference on Ubiquitous Intelligence and Computing (UIC2009)</td>
</tr>
<tr>
<td>37</td>
<td>Topical Workshop on Heterostructure Microelectronics (TWHM)</td>
</tr>
<tr>
<td>38</td>
<td>International Symposium on Nonlinear Theory and its Applications (NOLTA)</td>
</tr>
</tbody>
</table>
8. Periodicals Published by the Institute

The Institute publishes the following two periodicals to inform readers on recent research results of the Institute.

1. The Record of Electrical and Communication Engineering Conversazione Tohoku University

This journal aims at providing an opportunity to publish research results of the Institute as well as the result of the Departments of Electrical Engineering, Communication Engineering, Electronics Engineering, and Information Engineering of the Faculty of Engineering. Since the journal also aims at publishing general research activities of the Institute and of the Departments such as records of the final lectures of retiring professors, records of the Institute Symposium, and reviews.

The name of the Journal ‘Conversazione’ is attributable to the ‘Tuesday Conversazione’ at the Department of Electrical Engineering, which had been held once a week on Tuesday since around 1920. Minutes of the meetings had been distributed to researchers outside of the University via various routes and therefore some of them had been referred to as ‘Records of Tuesday Electrical Engineering Conversazione Tohoku University’ with the result that they came to be treated as official publications.

Though the meeting was once interrupted by World War Two, it was restarted in 1947. In 1952, the publication of the records was succeeded by the Institute and the records have been published as periodicals, two or three times a year recently, since No. 1 Vol. 21 was published in July, 1952.

2. The Annual Report of Research Activity at the Research Institute of Electrical Communication, Tohoku University

Published annually since 1995. This report details the activities of each research division and research facility. Also included are reports on nation-wide co-operative research projects, international symposium and seminars organized by members of RIEC, and the reports and evaluation on the RIEC advisory board members. English edition (digest version of Japanese edition) has been published since 2007.
9. Staff, Land and Buildings, Budget

1. Staff

<table>
<thead>
<tr>
<th>Classification</th>
<th>Division</th>
<th>Laboratory for Nanoelectronics and Spintronics</th>
<th>Laboratory for Brainware Systems</th>
<th>Research Center for 21st Century Information Technology</th>
<th>Fundamentals Technology Center</th>
<th>Administration Office</th>
<th>(2008.8.1)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Professors</td>
<td>19</td>
<td>3</td>
<td>4</td>
<td>3</td>
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<td>Head Official</td>
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<td>Associate Professors</td>
<td>12</td>
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<td>1</td>
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<td>General Affairs Group</td>
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<td>Assistant Professors</td>
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<td>Research Fellows</td>
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<td>7</td>
<td>2</td>
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<td>Technical Officials</td>
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<td>Administrative Officials</td>
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<td>13</td>
<td>5</td>
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2. Land and Buildings

Site: Katahira 2-1-1, Aoba-ku, Sendai 980-8577, Japan

Total building area: 12,913m²
Total floor area: 28,776m²

<table>
<thead>
<tr>
<th>Name of Buildings</th>
<th>Structure</th>
<th>Year of Completion</th>
<th>Floor Area</th>
</tr>
</thead>
<tbody>
<tr>
<td>Building No.1</td>
<td>Reinforced Concrete, 4 floors</td>
<td>Building-S: 1962, 1963</td>
<td>7,772m²</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Building-N: 1959, 1960</td>
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</tr>
<tr>
<td>Building No.2</td>
<td>Reinforced Concrete, 4 floors</td>
<td>1962, 1963</td>
<td>7,085m²</td>
</tr>
<tr>
<td>Laboratory for Nanoelectronics and Spintronics</td>
<td>Steel-frame, 5 floors</td>
<td>2004</td>
<td>7,375m²</td>
</tr>
<tr>
<td>Laboratory for Brainware systems</td>
<td>Reinforced Concrete, 1 floor</td>
<td>1967, 1968, 1972</td>
<td>525m²</td>
</tr>
<tr>
<td></td>
<td>Reinforced Concrete(partly steel-frame), 2 floors</td>
<td>1986</td>
<td>1,553m²</td>
</tr>
<tr>
<td></td>
<td>Steel-frame 1 floor</td>
<td>1996</td>
<td>598m²</td>
</tr>
<tr>
<td></td>
<td>Light-weight steel-frame, 2 floors</td>
<td>1999</td>
<td>147m²</td>
</tr>
<tr>
<td>Research Center for 21st century Information Technology</td>
<td>Reinforced Concrete, 3 floors</td>
<td>1930</td>
<td>1,343m²</td>
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<tr>
<td></td>
<td>Steel-frame 1 floor</td>
<td>2002</td>
<td>435m²</td>
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<td>Evaluation and Analysis Center</td>
<td>Reinforced Concrete, 2 floors</td>
<td>1981</td>
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<td>Helium Sub-Center</td>
<td>Reinforced Concrete(partly light-weight steel-frame), 1 floor</td>
<td>1972</td>
<td>166m²</td>
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<td>Machine Shop</td>
<td>Reinforced Concrete(partly light-weight steel-frame), 1 floor</td>
<td>1965, 1966, 1978</td>
<td>479m²</td>
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<td>Others</td>
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<td>Total</td>
<td></td>
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<td>28,776m²</td>
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3. Budget

(Unit: 1,000 yen)

<table>
<thead>
<tr>
<th>Financial Year</th>
<th>Personnel Expenditure</th>
<th>Supplies Expenditure</th>
<th>Research Grant</th>
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</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td>Ministry of Education, Science and Culture</td>
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<tr>
<td>2004</td>
<td>902,978</td>
<td>1,233,357</td>
<td>338,459</td>
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<td>2005</td>
<td>984,113</td>
<td>1,050,647</td>
<td>554,680</td>
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<tr>
<td>2006</td>
<td>971,482</td>
<td>927,090</td>
<td>599,040</td>
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<td>2007</td>
<td>970,961</td>
<td>813,724</td>
<td>700,615</td>
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<tr>
<td>2008</td>
<td>879,481</td>
<td>953,000</td>
<td>694,883</td>
</tr>
</tbody>
</table>
10. Afterword

The state of universities closely depends on the social situation. The modern civilizations have been mainly supported by the modern science and technology, which have been developed by the concept of the subject-object separation, which is called Cartesian dualism. That is, nature was cordoned off from human beings and objectified. What is important here is that nature was cut off and one phenomenon seemed to occur independently from others. It leads us to rid ourselves of all of nature’s complexity and makes it extremely simple. In other words, we attempted to find rules of an ideal world that we can regard nature as being homogeneous.

What was essential was that these isolated worlds would not interfere with each other. If so, by summing up the analytical worlds that were sought, the whole world could be constructed in its entirety. These traditional approaches were rather easy to understand, and because of that, they were accepted rather widely. By pursuing this approach, the natural sciences have acquired a universality and predictability, while evolving into a powerful methodology. But anyway, the modern society was constituted by this approach to the natural sciences. By working together in the natural science and the engineering, we have created a society with the objective of making it highly affluent.

The problem, however, is that this separation of self and other has been applied to the realm of human activities as well. This means an objectification of the various human activities themselves. Against this objectified activity, an objective of self-completion is established. From what we have heard here today, in business and academia as well, the goal is self-completion. There is no existing theory that would apply limits to the objective of attaining self-completion itself. That is why, for example, if it takes the form of the pursuit of profit, then profit is pursued without limit.

The doctrine of competition on the world market has spread all over the world, involving Japan as a matter of course. For impartial competition, it requires liberalization and internationalization of trade, and information technology for higher efficiency. That is the entity of the globalization. Thus the modern science and technology has brought about great change of social structure and industrial structure, from age of ideological opposition to age of economical competition.

Especially, information science and communication technology have rapidly developed, which are great useful tools to win in competition on the world market. Particularly in the USA, the information technology has been adopted very early in economic activities. In consequence, the USA has been the top runner in the world
market. In modern society to promote efficiency is a crucial factor of a struggle for existence in the world market. Now the information technology intensifies the competitive nature of present-day market.

The market principle has been brought into the universities, that is, the university is evaluated by the market value. To market universities, it is necessary to evaluate the current states of universities numerically. Fundamentally it is, however, impossible to estimate the quality of the research and education in numbers. So there are great limitations for the numerical estimation of the universities. Originally the universities are assigned the essential role of inheriting and creating culture. It is needless to say that the universities of our country should contribute not only to the current prosperity of our country but the future one. For this purpose it is necessary to raise further the cultural level of our nation, which is exactly the mission of the universities. Now we must take leave to the age of endless materialistic desires,” More is better”, and open the age of fine quality of life, “Enough is best”.

We have philosophically redefined in 2001 what RIEC should be. “It is possible for human society to sustainably develop, only when a barrier-free communication is guaranteed. Devoting ourselves to the research of information science and communication technology, we will contribute to the prosperity and development of our own country, and extensively to the progress of the culture and welfare of mankind.” All the staff in our institute will assume the responsibility to information and communication systems in Japan, and each research activity should be approved by the nation. Academic freedom of how to approach the problems is a right of all researchers, but each is accountable for his/her own study. For doing our duty, we need to show what we are doing and what the future of the information science and communication technology will be. This annual report presents our daily research activities, so I greatly appreciate your frank and critical comments on our activities.